



Agenda

13th Power Analysis & Design Symposium

April 17th, 2024



	Central European Summer Time (CEST/UTC +2) (Vienna, Berlin)	Eastern Daylight Time (EST/UTC-4) (New York)	Hong Kong Time (HKT/UTC+8)
<i>Welcome and introduction</i>	09:00 / 09:00 am	03:00 am	15:00 / 03:00 pm
An Introduction to Power Supply Simulations with SIMPLIS by Christophe Basso - Future Electronics	09:10 / 09:10 am	03:10 am	15:10 / 03:10 pm
<i>10 min break</i>			
Using S-Parameters for the Design of EMC Filters by Arturo Mediano - University of Zaragoza	10:20 / 10:20 am	04:20 am	16:20 / 04:20 pm
<i>10 min break</i>			
Designing Digital Control Loops and Firmware by Andreas Reiter - Microchip Technology	11:20 / 11:20 am	05:20 am	17:20 / 05:20 pm
<i>1.5 h break</i>			
A Power Supply Filter with On-board Voltage Socket for 12V 10A by Günther Klenner - K&K Prime	13:40 / 01:40 pm	07:40 am	19:40 / 07:40 pm
<i>10 min break</i>			
PDN Impedance Measurements using the Bode 500 and Picotest PDN Probes by Florian Hämmerle - OMICRON Lab	14:40 / 02:40 pm	08:40 am	20:40 / 08:40 pm
<i>10 min break</i>			
Power Supply Compensator Design Using Operational Transconductance Amplifiers by Dr. Ali Shirsavar - Biricha Digital	15:40 / 03:40 pm	09:40 am	21:40 / 09:40 pm
<i>Discussion & Closing</i>			