

# **TICRO 100 - Application Note**

# Time Synchronization of protection relays to IEEE 1588/PTP



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Smart Timing Solutions

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- Note: Detailed settings of the TICRO 100 are explained in the TICRO 100 user manual. Download the TICRO 100 user manual at: <u>http://www.omicron-lab.com/ticro-100/downloads.html</u>
- Note: All SW settings are done with TICRO firmware version ticro100-1.01.0013 or higher. Download the latest firmware at: <u>http://www.omicron-lab.com/ticro-100/downloads.html#2</u>
- **Note**: The Grandmaster clock and the infrastructure have to be IEEE 1588/PTPv2 capable. We used the OTMC 100 as PTP Grandmaster clock. Download additional information at: http://www.omicron-lab.com/otmc-100/product-description.html



# 1 Executive Summary

This Application Note shows how to integrate non PTP compliant IEDs into IEEE 1588/PTPv2 time synchronization infrastructures. In detail it shows, how to synchronize ABB Protection Relays<sup>1</sup> with optical synchronization inputs using the TICRO 100 PTP Time Converter.

The following topics are covered:

- Basic settings at the TICRO 100 and the RET670
- Verification if the relay has been successfully synchronized
- Selection of the correct PTP network settings

# 2 Technical Background

The North American Blackout back in August 2003 visualized how painful and time consuming it can be to align data, whose time stamps are derived from inaccurate time references. As a result the task force investigating the blackout demanded a regulation that ensures a minimum absolute accuracy for time stamped disturbance event data. With the adoption of the NERC<sup>2</sup> Standard PRC-018-1 in 2006 it is now a legal obligation that all recorded data has to have an accuracy of 2 ms or better in relation to UTC<sup>3</sup>. (see [1], page 1)

Especially with the integration of Smart Grids, the standard IEC 61850 becomes important. In the IEC 61850-9-5 standard, the time accuracy requirements for time tagging of events and time synchronized measurements are summarized in five time performance classes T1 to T5 which range from 1 ms to 1  $\mu$ s. (see [1], page 2)

## 2.1 The IRIG-B Time code

The IRIG Time Codes were developed in 1960 by a working group of the US Military to allow standardized time distribution and synchronization of technical equipment. The IRIG-B Code transmits the time information in a 100 bit telegram every second. A synchronization accuracy between 1 ms to 10  $\mu$ s can be achieved. For optical links unmodulated IRIG-B signals<sup>4</sup> are used.

## 2.2 Precision Time Protocol (PTP) IEEE 1588-2008

The Precision Time Protocol (PTP) was developed in 2002 to achieve a higher synchronization accuracy over Ethernet than the one so far provided by NTP. With NTP synchronization accuracies in the range from 10 ms to 1 ms can achieved. This is not sufficient for the synchronization of all technical equipment in a substation. Therefore protection equipment is currently controlled and monitored via Ethernet, but synchronized via a parallel system like IRIG-B.

<sup>&</sup>lt;sup>4</sup> IRIG-B: Inter Range Instrumentation Group Format B ([2], page 4-1, Figure 4-1)



<sup>&</sup>lt;sup>1</sup> In particular an ABB RET670 is used.

<sup>&</sup>lt;sup>2</sup> NERC: North American Electric Reliability Cooperation

<sup>&</sup>lt;sup>3</sup> UTC: Universal Coordinated Time Scale

PTP distributes the time synchronization information also via Ethernet, the achievable accuracy is in the 100 ns range. Contrary to IRIG-B no additional wiring is required.

PTP uses Ethernet and therefore has the advantage, that all redundancy features of Ethernet networks can be used. Using two grandmaster clocks is an easy and effective method to increase the availability of the time synchronization to all clients. Additionally the best master clock algorithm (BMCA) offered by PTP ensures that always the best available master clock in the network is used to synchronize the connected equipment. More details can be found in [1], page 5-6.

## 2.3 Why to use the optical input of the RET670

There are different reasons to use the optical input of the IRIG-B time synchronization module of the RET670. All of this reasons are independent of the use of the TICRO 100, since the TICRO 100 supports electrical, opto coupler and optical outputs (see [3], page 4).

The main reasons to use the optical interface are its reliability and immunity against EMI<sup>5</sup>. The optical link can cover a cable length from a few meters up to 2 km, using 62.5/125 µm multimode fiber, without any restriction regarding the signal quality. In comparison an electrical IRIG-B link is limited to 100 m using unmodulated signals or 300 m using modulated signals. In a substation setup the optical fiber can cover any ordinary distance with a single link.

The optical fiber offers a perfect insulation between the transmitter and receiver of the time synchronization network.

There is only one restriction which should kept in mind: The average signal delay across an optical or electrical link is about 5 ns/m.



# 3 Application Setup

The following chapter covers a typical time synchronization setup with the TICRO 100 and the RET670 using the RET670's optical time sync input. It shows how to connect the relay to the TICRO 100, the necessary steps to configure the TICRO 100 and the RET670.

# 3.1 Block diagram

The used setup consists of an OMICRON Lab OTMC 100 PTP Masterclock, a PTP compliant Ethernet switch, a PC, the TICRO 100 PTP time converter and the RET670 protection relay.

The PTP Masterclock receives its reference time from GPS and maintains an accuracy of  $\pm 100$  ns in comparison to UTC while locked to GPS. All connected PTP compliant equipment like the PTP switch and the TICRO 100 are synchronized via Ethernet. The achieved accuracy for a simple setup like this is in the range of better than  $\pm 200$  ns. The TICRO 100 generates the IRIG-B signal to synchronize the RET670 and a 1PPS-Signal to synchronize a merging unit.



Figure 1: Block Diagram of PTP setup

The PoE Injector, the PTP Switch, the TICRO 100 and the RET670 are isolated from each other, since the data connection is done by optical fiber. The PoE-Injector supplies the OTMC 100 PTP Masterclock (outside the building) with electrical power via an electrical RJ45 Ethernet connection. The distance between PoE-Injector and OTMC 100 can be up to 100 m the distance between PoE-Injector and PTP-Switch is optical and could be up to 2 km without any restrictions.



# 3.2 Connecting the RET670 to the TICRO 100

The optical time synchronization input of the RET670 is located at the rear side of the IED as shown in Figure 3. For this application the first optical output of the TICRO 100 was connected with a  $62.5/125 \mu m$  multimode fiber (with red marking) to the RET670. The second optical output was used to provide a 1pps signal to synchronize a merging unit. (See Figure 2)



Figure 3: Time Synchronization Input of RET670



Figure 2: Connecting RET670 with TICRO 100

# 3.3 PTP Settings of the TICRO 100

To ensure proper operation all PTP components in the network including the TICRO 100 need to be configured to the same PTP-Mode. In this section all PTP settings for the TICRO 100 are shown. Setup information for the remaining PTP components like the PTP switch and the PTP master clock can be found in Appendix A.

The PTP master clock defines the settings for all other components inside the PTP system. A typical PTP-Mode for applications in substations is the PTP profile for power systems described in IEEE C37.238-2011<sup>6</sup>. The TICRO 100 can be easily set-up via its web interface as shown on the next page.<sup>7</sup>

<sup>&</sup>lt;sup>7</sup> For the PTP settings applied to the TICRO 100 default settings where chosen, this values might be different for other PTP networks.



<sup>&</sup>lt;sup>6</sup> IEEE Standard Profile for use of IEEE 1588 Precision Time Protocol in Power System Applications

Navigate to Configuration/PTP and apply the following settings in the respective Tabs:

#### Tab General (Figure 4):

- PTP profile: Power systems
- Max. GM inaccuracy: Up to 1 µs (optional)

General Default	Port		
PTP profile:	Power systems	$\supset$	
User description:	TICRO ABB RET670		
Max. GM inaccuracy	/: Up to 1µs		

Figure 4: General PTP settings of the TICRO 100

**Remark:** The max. GM inaccuracy can be set, to ensure the TICRO 100 switches to its internal oscillator in case that the Grand master clock inaccuracy exceeds the set limit.

#### Tab Default (Figure 5):

- domain number: 0

General Default Port			
Domain number:			
Engineered network time inaccuracy:	0	ns	
Local time inaccuracy:	0	ns	

Figure 5: Default PTP settings of the TICRO 100

#### Tab **Port** (Figure 6):

- Transport: IEEE 802.3
- VLAN Id: 0
- VLAN PCP: 4

General Default Port			
Transport:	IEEE 802.3		
VLAN Id:	0		
VLAN PCP:	4		
Log sync interval:	0	2 <sup>x</sup> s	
Log min pdelay request interval:	0	2 <sup>x</sup> s	
Log announce interval:	0	2 <sup>x</sup> s	
Announce receipt timeout:	3		

Figure 6: Port PTP settings of the TICRO 100

**Note:** All not mentioned parameters in the figures above are not changed from their default values. The setting of these parameter are optional.



# 3.4 IRIG-B Settings of the TICRO 100

This section shows how to configure the optical outputs of the TICRO 100 to provide the signals required by the connected equipment.

## 3.4.1 Setup of Output 4 (Fiber)

The Output 4 (Fiber) connected to the RET670 needs to be configured as IRIG-B output.

Navigate to Configure/Output and apply the following settings in the respective Tabs:

**Θ 4 (Fiber)** (Figure 7):

 Time base: UTC
 Mode: IRIG-B
 Code expressions: Control functions Off Straight binary seconds Off BCD<sub>YEAR</sub> On
 These settings represent IRIG-B 006.

G+1 (BNC) G+2 (BNC) G	→ 3 (OC)
Time base:	UTC •
Mode:	IRIG-B 🔹
Coded expressions:	Control functions
	Straight binary seconds
	✓ BCD <sub>YEAR</sub>
Output muting:	Output muted

Figure 7: Sets the Fiber Output 4 of the TICRO 100 to IRIG-B

**Note:** Select the check boxes for straight binary seconds and/or control functions to select different IRIG-B00x codes.

## 3.4.2 Setup of Output 5 (Fiber)

The merging unit connected to Output 5 (Fiber) requires a 1PPS pulse. In the following example a pulse width of 10 ms is used.

Navigate to Configure/Output and apply the following settings in the respective Tabs:

**Θ 5 (Fiber)** (Figure 8):

- Time base: UTC
- Mode: PPX
- Pulses per second: 1 PPS
- Pulse width: 1000000 ns
- Time reference: Rising edge

G+1 (BNC)	G+ 2 (BNC)	G+3 (0C)	G+4 (Fiber)	G+ 5 (Fiber)		
Time base:			UTC		•	
Mode:			PPX		٠	
Pulses per sec	ond:		1 PPS		۲	
Pulse width:			1000000			ns
Time reference	e:		Rising edge		•	
Output muting	g:		🗌 Output mute	ed		

Figure 8: Sets the Fiber Output 5 of the TICRO 100 to 1PPS

**Remark:** The IRIG-B pattern and the 1 PPS pulse are both in line with UTC. Therefore the data acquisition of the merging unit will be in line with the system time of the IED.



#### 3.4.3 Output overview

All settings can be monitored at: Status/Output/Overview (Figure 9)

	CRO 100 Time Termina	I		
		Overview G-1 (i	3NC) 🕞 2 (BNC) 🕞 3 (OC) 🕞 4 (Fiber)	Help - Support - Manual - Logout
Overview	Output	G= 10 MHz: G= 1 (BNC): G= 2 (BNC): G= 3 (OC):	<ul> <li>Active</li> <li>IRIG-B006</li> <li>1 PPS</li> <li>1 PPS</li> </ul>	edit edit edit
Status	Network	G+4 (Fiber): G+5 (Fiber):	<ul> <li>IRIG-B006</li> <li>1 PPS</li> </ul>	edit edit
	Log Viewer	Clock Informatic Device status: Outputs active: Current timezone: OCXO tuning value:	Locked When locked or in hold over Europe/Berlin 45.46 %	

Figure 9: Overview of the TICRO 100 output settings

On the overview page the status of all outputs is visualized. If an output is active, a green indicator is shown. In addition the **Device status** of the TICRO 100 is shown. When all PTP settings match, the device status will be **Locked**.

## 3.5 IRIG-B Settings at the RET670 from the front panel

To set up the RET670 the HMI (Human Machine Interface) on the front panel of the IED can be used. It is also possible to use the remote software PCM600 to set the parameter for time synchronization. After startup of the RET670 navigate to the following menus and change the parameters as stated in the following tables. (Table 1, Table 2)

/RET670/Settings/Time/Synchronisation/TIMESYNCHGEN: 1/General (Figure 10)

Parameter Name	Value	Description
CoarseSyncSrc	OFF	Coarse time synchronization source
FineSyncSource	IRIG-B	Fine time synchronization source
SyncMaster	OFF	Activate IED as synchronization master
TimeAdjustRate	Fast	Adjust rate for time synchronization

Synchronisation/	IMESYNCHGEN:1/General
CoarseSyndSro	Off
FineSuncSource:	IRIG-B
SuncMaster :	Off
TimeAdjustRate:	Fast
and the second	

Figure 10: Settings of TIMESYNCHGEN: 1 at the HMI



Table 1: settings of the parameter group

TIMESYNCHGEN/General (See [4], page 81)

Parameter Name	Value	Description
SynchType Opto		Type of
		synchronization
TimeDomain	LocalTime	Time domain
Encoding	IRIG-B	Type of encoding
TimeZoneAs1344	PlusTZ	Time zone as
		in 1344 standard

/RET670/Settings/Time/Synchronisation/SYNCHIRIG-B: 1	(Figure 1 <sup>4</sup>	1)
	(i iguio i	• /

Time/Synchronisa	ation/SYNCHIRIG-B:1
SynchType 🔪 🔪 :	Opto
TimeDomain :	LocalTime
Encoding :	IRIG-B
TimeZoneAs1344:	PlusTZ

Table 2: Settings of the parameter group **SYNCHIRIG-B** of the RET670 (See [4], page 83)

Figure 11: Settings of SYNCHIRIG-B: 1 at the HMI

To use the full functionality of the synchronization inside the IED, the following parameters can be set to process additional data information given by IRIG-B (Table 3):

### /RET670/Settings/Time/Synchronisation/TIMESYNCHGEN: 1/IEC61850-9-2 (Figure 12)

Parameter Name	Value	Description
HWSyncSrc	IRIG-B	Hardware time
		synchronization source
AppSynch	Synch	Time synchronization
		mode for application
SyncAccLevel	Class T5	Wanted time
	(1us)	synchronization accuracy

IMESYNCHGEN: 1	/IEC	61850-9-	2	
				RIG-B
AppSynch SyncAccLevel	:	Class	T5	Synch (1us)

Table 3: settings of the optional parameter group **TIMESYNCHGEN/IEC61850-9-2** (see [4], page 81) Figure 12: Settings of IEC61850-9-2 at the HMI

For details on additional IED features refer to the Technical reference manual [4] of the RET670.



# 4 Checking successful synchronization

Once the parameters of the TICRO 100 and the RET670 are set, the time synchronization can be checked as follows at the HMI of the RET670:

Navigate the HMI of the RET670 to the menu: **RET670/Diagnostics/IED status/General** (Figure 13)

If the time synchronization via IRIG-B works correctly, you will see:

- INT Warning: Off
- Time Synch : Ready

More Details described at the Technical Reference Manual of the RET670. (See [4], page 69, figure 24)

/RET670/Diagnostics/IED	status/General
INT Fall	Off
CINT Warning	Off
NUM Pail	Uff
NUM Warning	Off
Time Synch	Ready
RIL	Ready
Application	
RTE	
IEC61850	Ready
LMD	Ready
BIM3	Ready
BOM4	Ready
ADM31	Ready

Figure 13: RET670 synchronized to IRIG-B Time Source

To test the synchronization, the optical connection between the RET670 and the TICRO 100 is disconnected. (See Figure 14)

After some seconds the parameter at the display will change to (Figure 15):

- INT Warning: On
- Time Synch : Fail



Figure 14: red IRIG-B ST Fiber disconnected

Figure 15: RET670 IRIG-B Signal lost, not synchronized

Figure 16: RET670 Event log

When the connection between the RET670 and the TICRO 100 is re-established and the synchronization works properly again the parameters in the display change back to normal operation. (See Figure 13)

The same behavior can also be monitored in the internal event log of the RET670. Navigate to **RET670/Diagnostics/Internal events** (see Figure 16) Here all events including the time synchronization are monitored. Figure 16 shows the synchronization events, when the optical connection is established (Off), disconnected (On) and reconnected (Off). All Events are time stamped by the RET670 with an accuracy of 1 ms. The advantage of the IRIG-B synchronization compared to other synchronization methods is, that a client only needs a few seconds to detect, verify and lock to the IRIG-B signal.



# 5 Summary

In this application note it was demonstrated how easy it is to integrate a non-PTP compliant IED into an IEEE 1588/PTPv2 infrastructure using the TICRO 100. The TICRO 100 can be easily configured via its web interface. It can be connected via RJ45 or the optical SFP Interface to the PTP network. Additionally it is possible, to configure the TICRO via USB without the need of any additional software. The TICRO 100 has 5 outputs which can be configured independent from each other.

The big advantages of the synchronization provided by the TICRO 100 are:

- Easy Integration of PTP and its advantage into existing installations. The TICRO 100 enables the introduction of PTP to existing installations without exchanging existing IEDs. This allows to benefit from the enhanced redundancy offered by the Best Master Clock Algorithm (BMCA) and the possible redundancy of a ring network topology.
- Enhanced monitor possibilities for all devices integrated into the timing network. All advantages of Ethernet can be used. Independent from the physical layer (optical or electrical) it is possible to check the status of all devices via their web interfaces.
- Holdover capability of the TICRO 100.

The TICRO 100 is equipped with an integrated OCXO, so it can still provide a highly accurate signal, even when the connection to the PTP network is broken for some time. Depending on the used OCXO the drift of the TICRO 100 in case of PTP signal loss is less than 25µs in 24 hours.

- Galvanic insulation between PTP-Network, TICRO 100, IED and Merging Unit. The possibility to connect the devices via optical fiber allows a full galvanic isolation between the used devices.
- Enhanced EMC of the Ethernet data connection.

The use of optical fiber for the data communication and time synchronization ensure that no electromagnetic emissions are caused by the data communication. In addition the optical Ethernet links are immune against electromagnetic disturbances.



# References

[1] 2010; IEEE1588 IMPLEMENTATION TRANSITION by B. Baumgartner, C. Riesch, M. Rudigier OMICRON electronics GmbH, Klaus, Austria; <u>http://www.omicron-lab.com/otmc-100/knowledge-applications/articles-use-cases/ieee-1588ptp-the-future-of-time-synchronization-in-the-electric-power-industry.html</u>

[2] 2004; IRIG SERIAL TIME CODE FORMATS Range Commanders Council, U.S. Army White Sands Missile Range, New Mexico 88002-5110 www.irigb.com/pdf/wp-irig-200-04.pdf

[3] 2013; TICRO-100 Brochure V1-1401 OMICRON Lab OMICRON electronics GmbH, Klaus, Austria; http://www.omicron-lab.com/fileadmin/assets/TICRO\_100/Brochures/TICRO-100\_Brochure\_V1-1401\_LR.pdf

[4] 2010; Transformer protection RET670 Technical reference manual ABB; Document ID: 1MRK 504 113-UEN



# Appendix

## A. PTP settings of the time synchronization system

To ensure proper operation all PTP devices in the network all devices need to be set to the same PTP-Mode. A typical PTP-Mode for applications in Substations is the PTP profile "Power Systems" as described in IEEE C37.238-2011<sup>8</sup>.

## a. PTP Settings of OTMC 100p

The PTP Grandmaster clock defines the settings for all other devices inside the PTP system. For this application note an OTMC 100p antenna integrated PTP Grandmaster clock was used.

Follow these steps to configure the PTP profile "Power Systems" via the web interface of the OTMC 100p.

Navigate to **Configuration/PTP** and apply the following settings in the respective Tabs:

Tab **General Settings** (Figure 17):

Tab Default settings (Figure 18)

Domain number:

Grandmaster ID:

\_

- PTP profile: Power Systems
- Operation mode: One step
- **Note:** Operation mode "One step" is the recommended mode since this mode produces less network traffic.

0

4

Note: The Grandmaster ID value 4 is a default

other PTP network components

value and was not changed. It can be

changed, but need to be changed at all



Figure 17: General Settings of OTMC 100



Figure 18: Port Settings of OTMC 100

<sup>8</sup> IEEE Standard Profile for use of IEEE 1588 Precision Time Protocol in Power System Applications



#### Tab Port Settings (Figure 19)

- Transport: IEEE\_802\_3

4

- Vlan ID: 0
- Vlan PCP:
- **Note:** The Vlan PCP value represents the Priority Code Point. It is equal to the Vlan priority.
- **Note:** All not mentioned parameters in the figures above are not changed from their default values. The setting of these parameters are optional.

ltern
2× s
2× s
2× s

Figure 19: Default Settings of OTMC 100

## b. PTP Settings of the Hirschmann RSP20

For this application note a RSP20 DIN rail switch from Hirschmann was used as PTP switch. In addition to the time synchronization the switch is also used for common data traffic.

This switch needs to be configured to the same PTP profile as the OTMC 100. It needs to be configured as a transparent clock according to IEEE 1588 PTP. Follow these steps to configure the correct PTP profile via the web interface of the switch:

Navigate to Section Time/PTP/Global and apply the following settings (Figure 20):

- Operation IEEE1588/PTP: On
- PTP Mode: v2-trandparent-clock

▲ ② ▶ 287 ₩ 287             ₩ 8 asic Settings	PTP Global Department 1588/PTP
ARP Table	● 0 n 0 off
Software	Configuration IEEE 1588/PTP
	PTP Mode v2-transparent-clock
	Supe Lawer Bound Inc. 20
Port Configuration	Sync Lower Bound Insj
Restart	Sync Upper Bound [ns] 5000
H     Security     in     Time	Enable PTP Management
Basic Settings     SNTP     PTP     Global	Status Is Synchronized Max Offset Absolute [ns] -801
Cock     Cock	PTP Time Jul 22, 2014 11:57:17 AM
<ul> <li>B Network Security</li> <li>B Switching</li> <li>B QoS/Priority</li> <li>B Redundancy</li> <li>B Diagnostics</li> <li>B Advanced</li> <li>B Help</li> </ul>	Set Reload I Help

Figure 20: Global PTP Settings of the PTP-Switch RSP20



#### Navigate to Section

Time/PTP/Transparent Clock/Global and apply the following settings (Figure 21):

0

4

- Delay Mechanism: P2P
- Primary Domain:
- Network Protocol: IEEE 802.3
- VLAN ID: 0
- VLAN Priority:
- **Note:** The settings above represent the same settings as set for the OTMC 100 PTP Masterclock. The delay mechanism P2P and the network protocol IEEE 802.3 are mandatory settings for the PTP profile "Power Systems", selected at the OTMC 100p. The VLAN Priority need to set to the same value like the parameter VIan PCP at the OTMC 100p (Figure 19)
  - Syntonize: On
  - Synchronize local clock: On

**Remark:** The settings Syntonize and Synchronize local clock are necessary to check and debug the PTP network settings.

8 🕹 🕲 🖟 265 🚽 😣	
Basic Settings	(h) HIRSCHMANN
System	Transparent Clock Global
P. Vetwork	Operation IEEE1588 / PTPv2 TC
Global	
ARP Table	Delay Mechanism O E2E O P2P O E2E-optimized O disabled
IP Address Conflict Detection	Primary Domain
Software	
Load/Save	Network Protocol IEEE 802.3
External Memory	Nu#i Damaia Mada
Port Configuration	
Restart	VLAN ID 0
i≢⊜ Security	VI AN Principy
	Volutional T
Basic Settings	Local Synchronisation
Brief SNIP	Syntonize
Global	Synchronize local clock
Boundary Clock	Current Master 20 b7 c0 ff fe 00 23 34 00 01
Dort	Offset To Master [ns] 9
Network Security	Delay To Master [ns] 0
E Switching	Statue IEEE1588 / DTDv2 TC
B QoS/Priority	
Redundancy	Clock Identity ec e5 55 ff fe 87 9c 7e
Diagnostics	
H Advanced	
🗄 😨 Help	Set Reload

Figure 21: Transparent Clock Settings of the PTP Switch RSP20

**Note:** The mentioned switch settings need to correspond with the settings of the PTP Grandmaster clock. If the settings of the network components in between the PTP Grandmaster clock and the connected PTP clients do not match, the PTP clients will not lock to the grandmaster clock.



#### c. Checking of successful PTP synchronization

Use the web interface to find out if the TICRO 100 has been successfully synchronized via PTP. When all settings have been applied correctly and the grandmaster clock is locked to GPS, the TICRO 100 becomes a PTP slave and its device status is locked.

#### Check on the TICRO 100:

Navigate to **Overview/General** of the web interface and check following settings. (Figure 22)

- Device status: Locked
- PTP: Slave

When the TICRO 100 has the PTP status Listening or Unlocked and does not start with the locking procedure most likely some of the settings mentioned in the previous section do not match to the grandmaster clock settings. To overcome this situation follow the extended check explained below:

#### Extended check on the TICRO 100:

First it needs to be verified if the used Grandmaster clock is locked to GPS and provides valid PTP packages. If this is the case, navigate to TICRO 100 **Status/PTP** section **Port** and check (Figure 23):

	· •	
-	Port State:	Slave

- Peer mean path delay: 3 ns<sup>9</sup>

An indication that the PTP settings match is when the parameter "Peer mean path delay" in section **Port** slightly changes. During PTP synchronization this value is never constant.

When the port state is Listening, Unlocked or Uncalibrated the following parameters Clock identity and Grandmaster identity should be checked.

Navigate to TICRO 100 **Status/PTP** and check (Figure 24). Section **Default** 

Clock Identity <sup>10</sup> :	20:b7:c0:ff:fe:00:3d:ad
Section <b>Parent</b>	

- Grandmaster identity<sup>10</sup>: 20:b7:c0:ff:fe:00:23:34

When both values are identical, the PTP settings of one or more components in the network do not match. In section **Parent** the parameter "Grandmaster identity" need to the same like the "Clock identity" of the Grandmaster clock. This clock identity can be checked at the Web interface of Grandmaster clock.

General	_
Device status:	Locked
PTP:	Slave
UTC date/time:	2014-07-22T14:13:27+0000
Product name:	TICRO 100
Serial number:	AL108R
Software version:	1.01.0013
Kernel version:	3.4.35
Hardware revision:	0002
Uptime:	4 days, 1 hours, 37 minutes

Figure 22: TICRO 100 Device status locked

Port	
Port state:	Slave
Delay mechanism:	P2P
Sync interval:	1 s
Announce interval:	1 s
Announce receipt timeout:	3
Minimum pdelay request interval:	1 s
Peer mean path delay:	3 ns
Delay asymmetry:	0 ns
Profile ID:	1c:12:9d:00:00:00
Network protocol:	IEEE_802_3
VLAN ID:	0
VLAN priority:	4

Figure 23: TICRO 100 Status PTP, Port

Default	
Clock identity:	20:b7:c0:ff:fe:00:3d:ad
Clock class:	SLAVE_ONLY (255)
Domain number:	0
Local time inaccuracy:	0 ns
Network time inaccuracy:	0 ns

Parent	
Port identity:	1@20:b7:c0:ff:fe:00:23:34
Grandmaster identity:	20:b7:c0:ff:fe:00:23:34
Grandmaster clock class:	PRIMARY_REF_PTP (6)
Grandmaster clock accuracy:	WITHIN_1_US (0x23)
Grandmaster clock variance:	18465
Grandmaster priority 1:	128
Grandmaster priority 2:	128
IEEE C37.238 grandmaster ID:	3
Grandmaster time inaccuracy:	0 ns
Network time inaccuracy:	0 ns

Figure 24: TICRO 100 Status PTP, Default & Parent

<sup>9</sup> The value depends on the network structure and slightly changes in a range of several ns.

<sup>10</sup> Value is different when other HW is used.



#### Extended check on the Grandmaster (OTMC 100):

The clock identity of the OTMC 100 can be checked via its web interface.

Navigate to **Status/PTP** tap **Default** (Figure 25):

- Clock Identity<sup>11</sup>: 20:b7:c0:ff:fe:00:23:34

Port	Default	Parent	Time	
Two step			false	
Clock identity			20:b7:c0:ff:fe:00:23:34	
Clock class			PRIMARY_REF_PTP (6)	
Clock accuracy			WITHIN_1_US (0x23)	
Figure 25: OTMC 100 clock identity				

In this example the TICRO 100 displays the same Grandmaster identity like the clock identity displayed by the OTMC 100.

#### Extended check on the PTP Switch (RSP 20):

When it has been verified that the settings at the TICRO 100 and the Grandmaster clock (OTMC 100) are correct, the next switch upstream of the TICRO 100 has to be checked.

In this application note a Hirschmann DIN rail switch RSP20 was used. This switch allows to check the grandmaster clock identity via its web interface.

#### Navigate to Time/PTP/Transparent Clock/Global

and apply following setting: (Figure 26)

- Syntonize: On
- Synchronize local clock: On

Check following parameter:

- Current Master<sup>11</sup>:20:b7:c0:ff:fe:00:23:34 00 01
- Offset To Master [ns]: 9 ns<sup>12</sup>

The last two Bytes 00 01 are added by the switch. So the Current Master is equal to the OTMC 100p.

Local Synchronisation Syntonize Synchronize local clock					
Current Master	20 b7 c0 ff fe 00 23 34 00 01				
Offset To Master [ns]	9				
Delay To Master [ns]	0				
Status IEEE1588 / PTPv2 TC					
Clock Identity ec e5 55 ff fe 87 9c 7e					
Set Reload					

Figure 26: RSP20 PTP Master Clock identity

<sup>&</sup>lt;sup>12</sup> The value "Offset To Master [ns]" will slightly change, when the "Reload" button is clicked.



<sup>&</sup>lt;sup>11</sup> Value is different when other HW is used.



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