



Agenda

11th Power Analysis & Design Symposium

March 9th, 2022



	Central European Time (CET/UTC +1)	Eastern Standard Time (EST/UTC-5)	Hong Kong Time (HKT/UTC+8)
<i>Welcome and introduction</i>	09:00 / 09:00 am	03:00 am	16:00 / 04:00 pm
The Danger of Underdamped Filters by Arturo Mediano - University of Zaragoza	09:10 / 09:10 am	03:10 am	16:10 / 04:10 pm
<i>10 min break</i>			
Automated PCB Parasitics Extraction from EDA Tools for Power Electronics Design by Sven Fießler - Technical University Ilmenau	10:00 / 10:00 am	04:00 am	17:00 / 05:00 pm
<i>10 min break</i>			
Practical System Modelling using Bench Measurements of Plant Transfer Functions by Andreas Reiter - Microchip Technology	10:50 / 10:50 am	04:50 am	17:50 / 05:50 pm
<i>1.5 h break</i>			
Impedances in DC-Microgrids - from Offline to Online Measurements by Raffael Schwanninger - IISB Fraunhofer	13:00 / 01:00 pm	07:00 am	20:00 / 08:00 pm
<i>10 min break</i>			
Step-by-Step Snubber and Clamp Design for Power Supplies by Dr. Ali Shirsavar - Biricha Digital	13:50 / 01:50 pm	07:50 am	20:50 / 08:50 pm
<i>20 min break</i>			
Comparing DC/DC conducted EMI Simulation with Measurement Results by Frank Puhane - Würth Elektronik & Markus Laudien - ANSYS	15:00 / 03:00 pm	09:00 am	22:00 / 10:00 pm
<i>10 min break</i>			
The Simple Truth about Complex Impedance Probes by Steve Sandler - Picotest	15:50 / 03:50 pm	09:50 am	22:50 / 10:50 pm
<i>Discussion & Closing</i>			