

The danger of underdamped filters!

OMICRON 11th Power Analysis & Design Symposium



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Organized by,

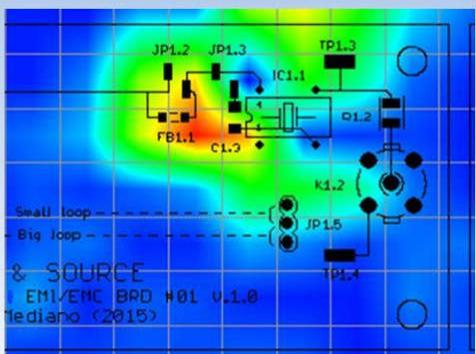


2022

March 9th, 2022



A High Frequency
Lab for
design, diagnostic,
troubleshooting and
training



Interferences (EMI)
Electromagnetic Compatibility (EMC)
Signal Integrity (SI)
Radiofrequency(RF)

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About the speaker ...

Prof. Arturo Mediano

Teaching Professor in EMI/EMC/RF/SI

I3A, University of Zaragoza (SPAIN)

Arturo Mediano is the founder of The HF-Magic Lab®, a specialized laboratory for design, diagnostic, troubleshooting, and training in the EMI/EMC, Signal Integrity, and RF fields at I3A (University of Zaragoza).

He received his M.Sc. (1990) and his Ph. D. (1997) in Electrical Engineering from the University of Zaragoza (Spain), where he has held a teaching professorship in EMI/EMC/RF/SI since 1992.

For more than 30 years Arturo has been involved in R&D projects with many companies in the EMI/EMC, Signal Integrity and RF fields for communications, industry, medical, and scientific applications. He regularly shares his knowledge and expertise with students and engineers in teaching courses and seminars.



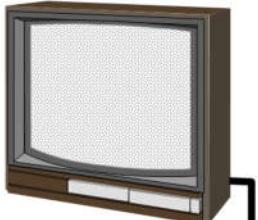
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Introduction: EMI picture (2)

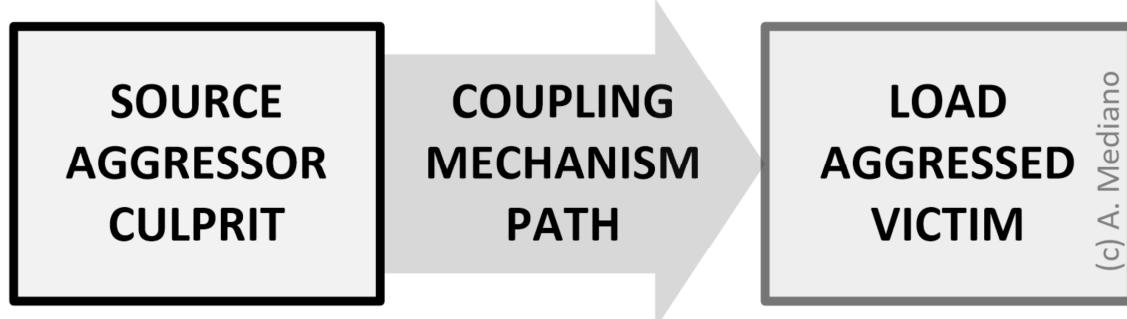


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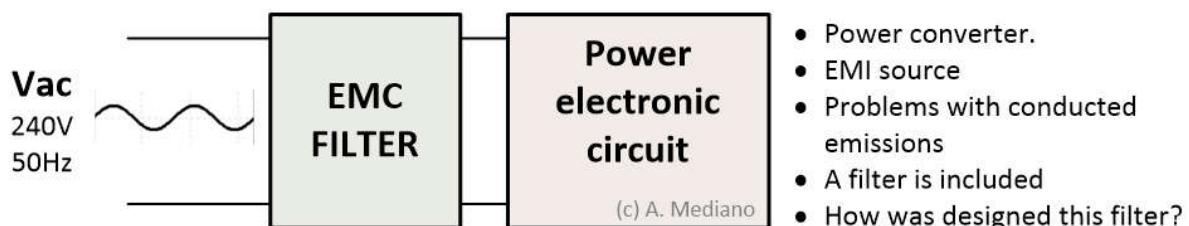
Introduction: EMI general picture



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The product: description

- 1.- Your product is a power electronic converter.
- 2.- Connected to mains network (i.e. 240V/50Hz)
- 3.- The product needs an EMC filter to comply with conducted emissions regulations.



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About the filter: how it was designed

How the filter was designed (possibilities):

- Option 1: same filter as from previous model (or custom tuned).
- Option 2: designed by trial an error → dangerous
- Option 3: direct replica from a competitor → honest?
- Option 4: commercial filter → expensive? (not always)
- Option 5: designed correctly from scratch → you need time

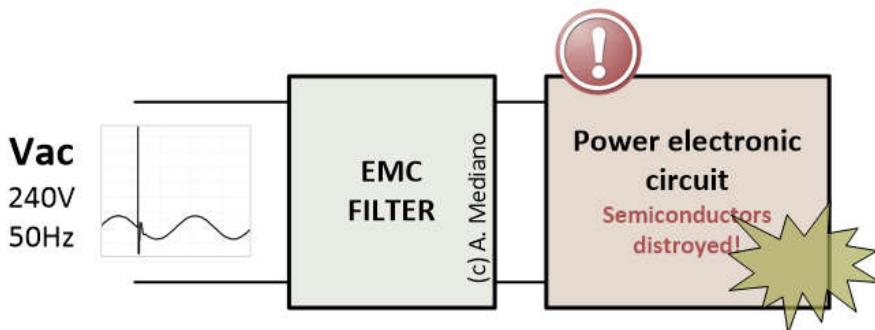
Additionally:

- designed only with emissions in mind (not immunity)
- not usually considering radiated emissions
- trying to use low cost components → be careful!



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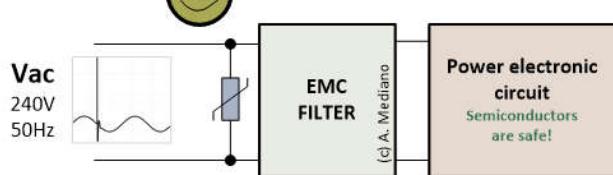
The product: trying to pass immunity



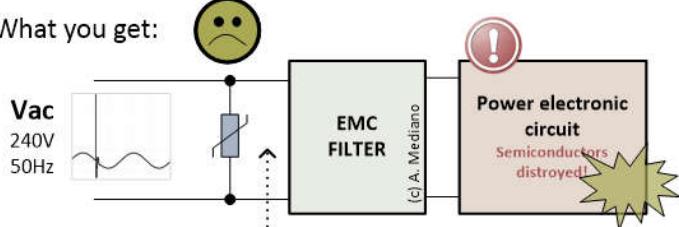
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Fail in immunity: varistor?

What you think:



What you get:



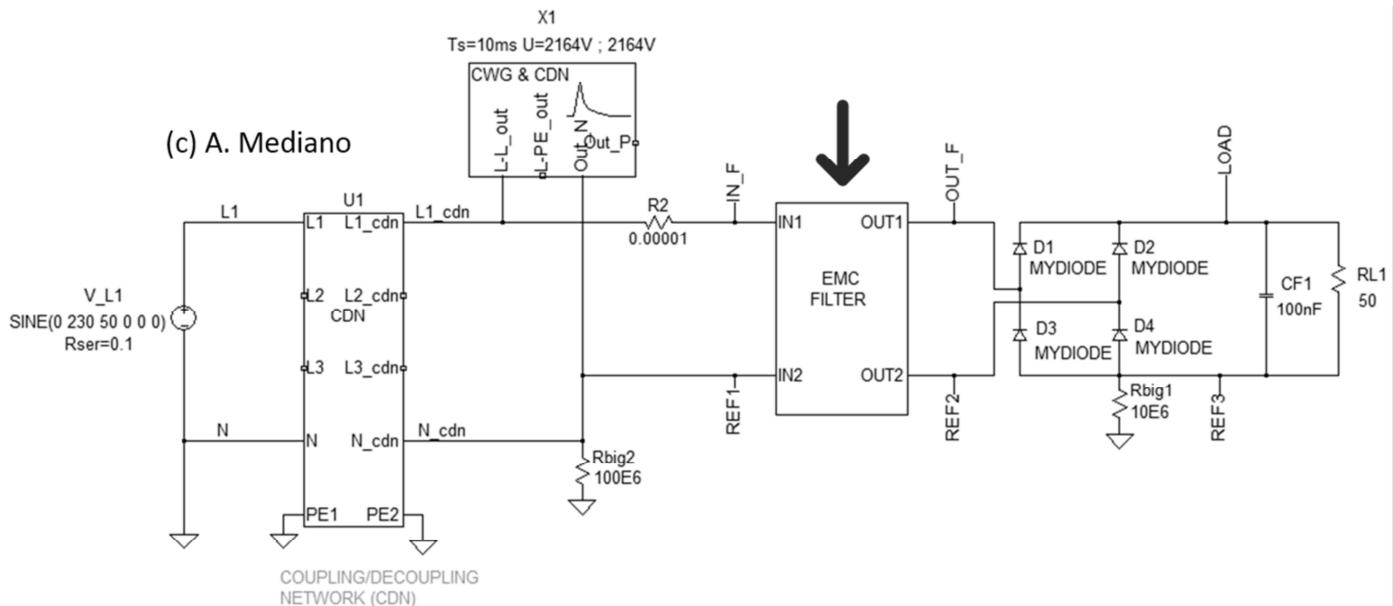
... and you can see varistor is clamping input transient!!!!

How is this possible??



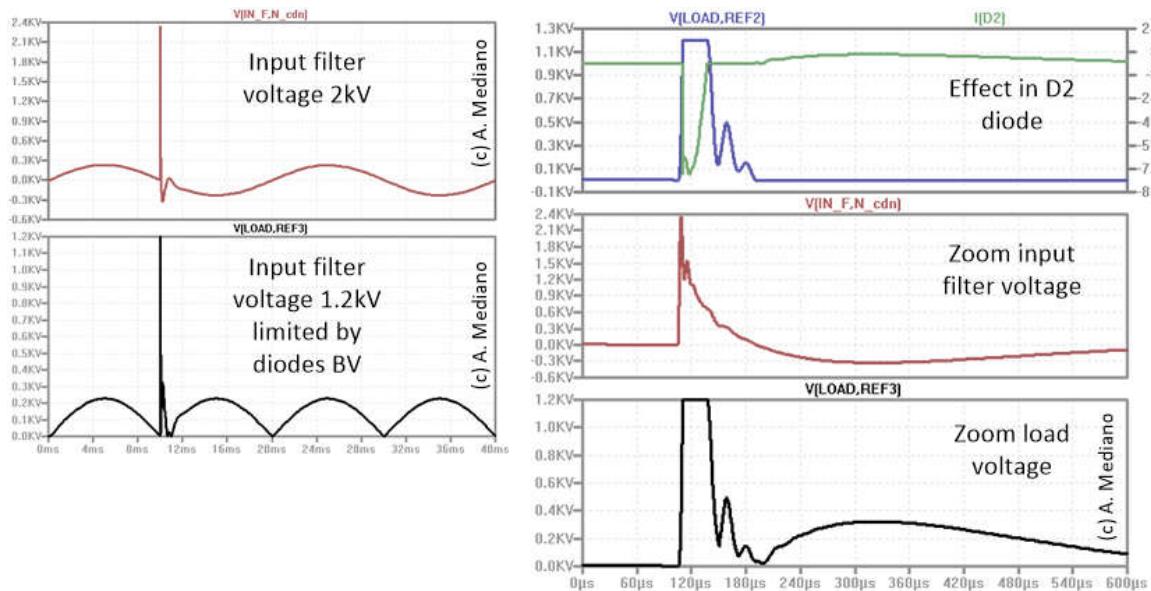
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Design example: with LTSPICE



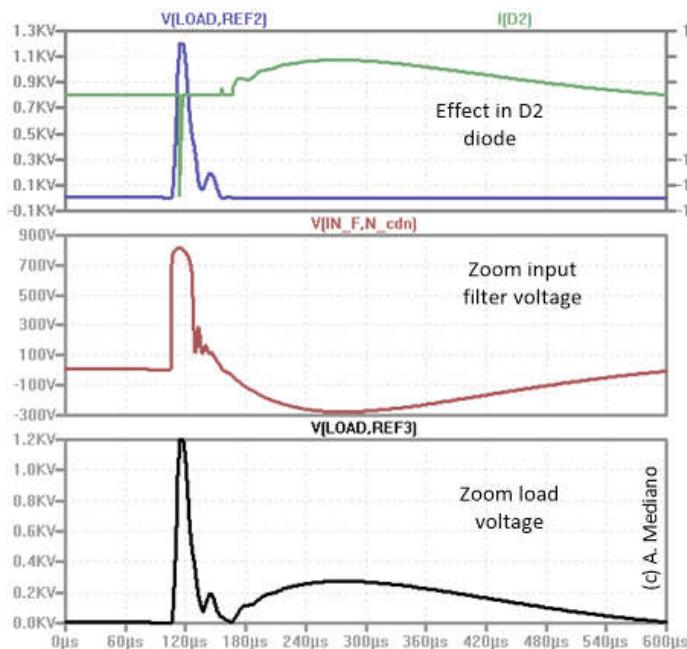
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Design example: with LTSPICE



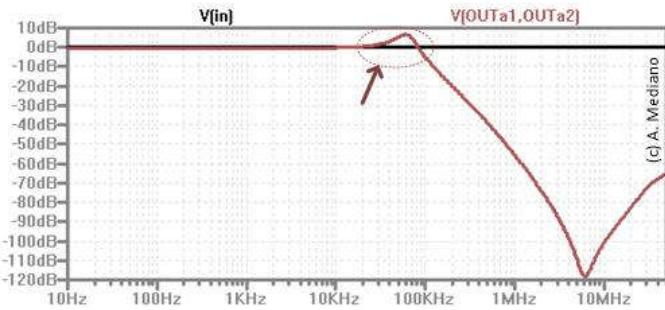
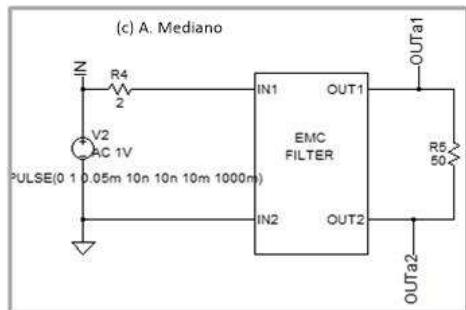
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Design example: adding input varistor

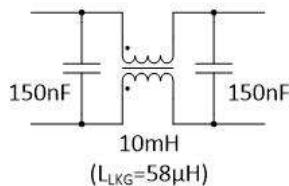


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Design example: filter review

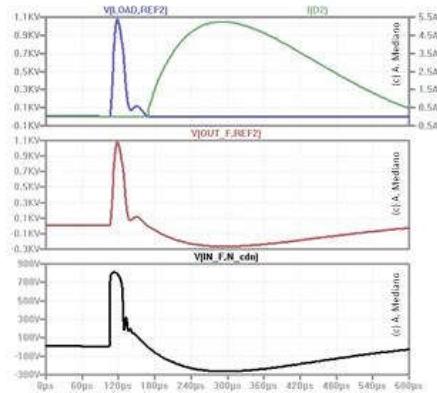
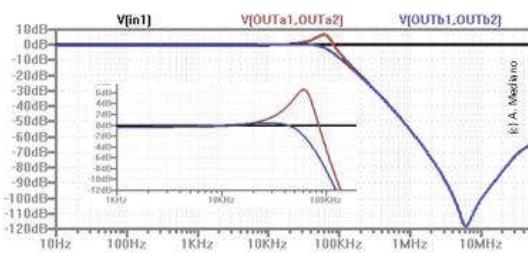
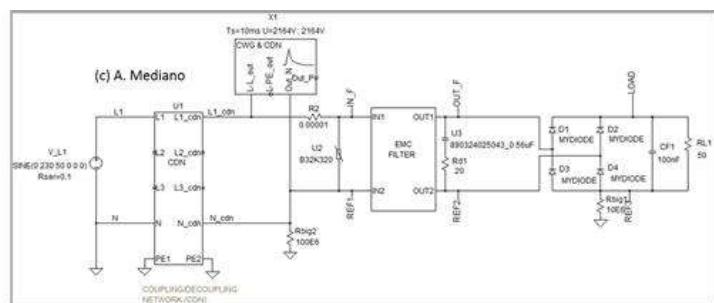
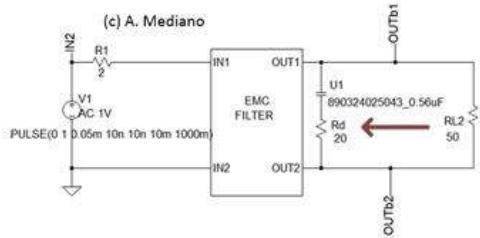


Basic schematic



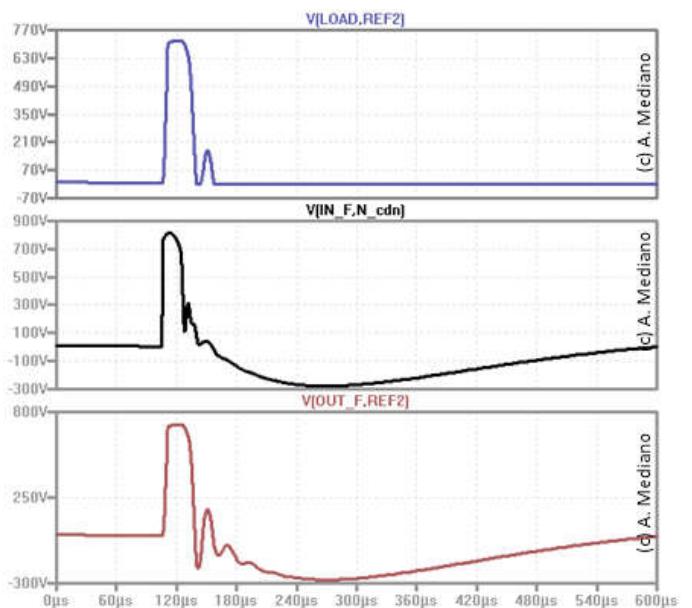
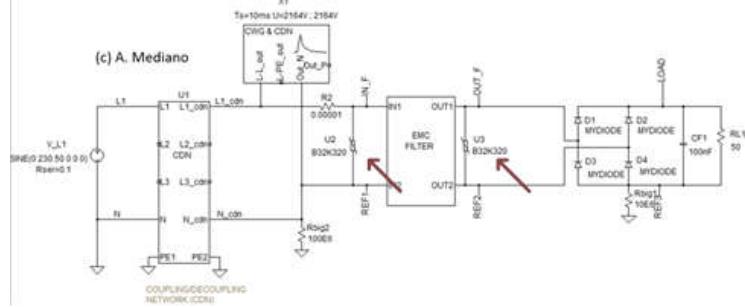
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Design example: filter damping



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Design example: using two varistors



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Demo



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THANK YOU!



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