




How to Use Off-the-Shelf Transformers in Switch Mode Power Supplies

Ali Shirsavar

Biricha Digital Power Ltd

© Biricha Digital Power Ltd
133 Rue Riverview
Rosemere
Quebec – J7A 2K1
Canada

1



Understanding Off-the-Shelf Transformers

- Each transformer comes with several windings (usually 6)
 - See Würth’s flex range of transformers
 - <https://www.we-online.com/en/components/products/WE-FLEX>
- The idea is that the user arranges these windings in either series/parallel or both to get the inductances and turns ratios that they need
 - This way you can get a transformer off-the-shelf at the expense of some performance
- Of course a generic transformer will not perform as well as a custom made one
 - Leakage will not be as good
 - May not have the exact turns ratio/inductance that you need
 - The pin out arrangement may not be ideal

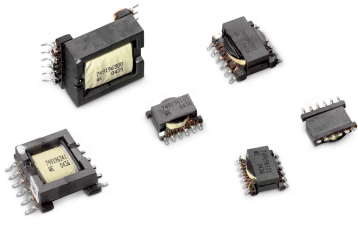


Image taken from: <https://www.we-online.com/en/components/products/WE-FLEX>

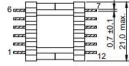
but wouldn't be great to order our transformer along with your other components from your favourite supplier?
We think this is possible for low voltage/low power Flyback applications (...convenience at the expense of some performance ...)

2



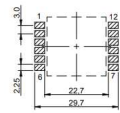
Example Flex Transformer

Dimensions: [mm]



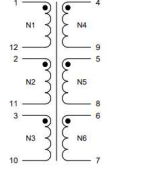
Scale - 1:1

Recommended Land Pattern: [mm]



Scale - 1:1

Schematic:



Electrical Properties:

Properties	Test conditions	Value	Unit	Tol.
Inductance Base ¹⁾	f_{res}	10 mH @ 1 V	3.4	μ H \pm 30%
Turns Ratio	n	1:1:1:1:1:1		
Rated Current Base ²⁾	I_{RMS}	$\Delta T = 40$ K	1.91	A typ.
Saturation Current Base ³⁾	I_{SAT}	$\Delta T < 10$ K	4.18	A typ.
DC Resistance Base ⁴⁾	R_{DC}	@ 20 °C	30	m Ω max.
Voltage-Induced Base ⁵⁾	V_{IND}	Unipolar waveform	65.0	μ Vs max.
Leakage Inductance Base	L_{leak}	10 mH @ 1 V	0.18	μ H typ.
Insulation Test Voltage	V_i	3 mA/1 s	500	V (DC)

¹⁾ Inductance of a single winding.
²⁾ Rated Current of a single winding.
³⁾ Saturation Current of a single winding when all windings are driven, if not all windings are driven, the Saturation Current per winding is $I_{SAT} \cdot \sqrt{\text{number of driven windings}}$.
⁴⁾ DC Resistance of a single winding.
⁵⁾ Voltage-Second of a single primary winding. For primary windings in series, multiple Voltage-Seconds, μ s, by the number of windings used. For primary windings in parallel, the Voltage-Second rating = Voltage-Seconds/age.

Certification:

RoHS Approval	Compliant (2011/65/EU;2015/863)
REACH Approval	Comform or declared (EC/1907/2006)

General Information:

Operating Temperature	-40 up to +125 °C
Storage Conditions (in original packaging)	< 40 °C; < 75 % RH
Storage Conditions (for single parts)	-40 up to +85 °C
Moisture Sensitivity Level (MSL)	1

Test conditions of Electrical Properties: +20 °C, 33 % RH if not specified differently

WURTH ELEKTRONIK
MORE THAN YOU EXPECT

WE-FLEX Flexible Transformer for DC/DC Converter

Part Number: 749196540

DATE: 2022-07-28

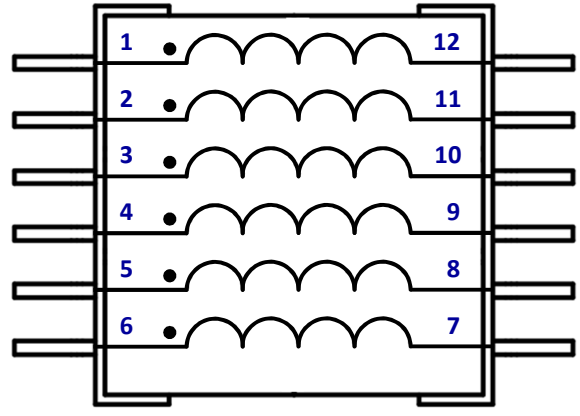
REV: 02

Datasheet taken from <https://www.we-online.com/components/products/datasheet/749196540.pdf>

- As can be seen from the datasheet, this range has 6 windings
- Product family can be found here - https://www.we-online.com/en/components/products/pbs/power_magnetics/file_xible_transformers_for_smtps
- We will soon show how to use these



Example Wurth Flex Transformer EFD15 & 20 Range*



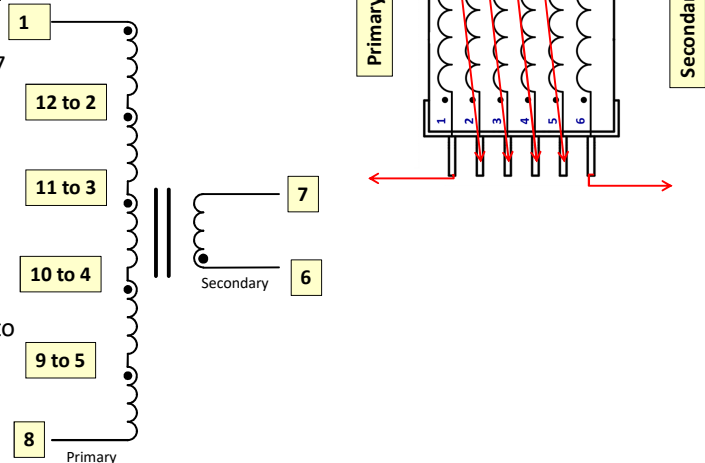
- Given this set up we can wire up our transformer to have a fixed set of turns ratios (N)

* Pinout for other sizes for other transformers may vary but the principle stays the same



Würth EFD20/15 Flex Transformer Example Turns Ratio ($N = N_p:N_s$)

- We have 6 coils and we can connect them any which way that we like
- If I use the coil connected between pins 7 and 6 as my secondary
- And connect pin 9 to 5, 10 to 4, 10 to 3 and 12 to 2
- Then 8 and 1 will be my primary
- And I will have a $N_p/N_s \rightarrow N$ of 5:1
 - N_p = Primary turns, N_s = Secondary turns
- Of course there are many ways of wiring to get the same N , however, we have used the current convention just for simplicity
 - This gives a clean layout at the expense of more leakage
 - If we interleave then we will have lower leakage but slightly messier layout

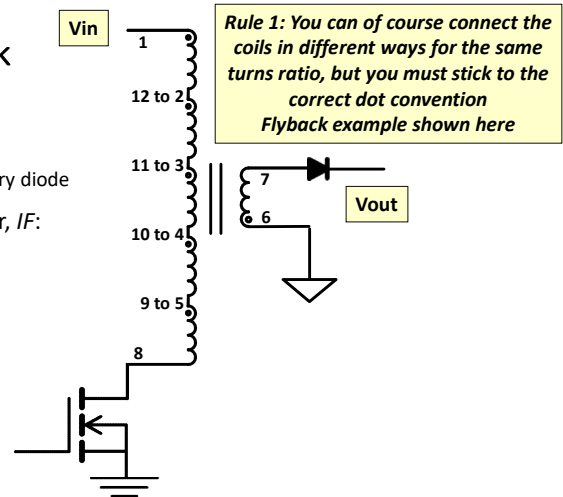


5



EFD20/15 Flex Transformer Example Flyback

- All we have to do now to get a DC-DC Flyback is to connect:
 - Pin 1 to V_{in} , Pin 8 to Drain of our switch
 - Pin 6 to secondary Ground and Pin 7 to the anode of the secondary diode
- Our choice is limited so we can use off-the-shelf transformer, IF :
 - We can get the right turns ratio (N)
 - We can get the right inductance
 - We can stay within the available Rated Current limit (RMS)
 - We can stay under our Saturation Current limit (Peak)
- Desired N and inductance can be easily calculated
 - from REDEXPERT or Biricha-WDS
- Actual N , inductance, rated and saturation currents can be
 - Easily read off Biricha Flex Transformer spreadsheet
 - Calculated from the device datasheet
- Biricha Flex Transformer Spreadsheet lists all of the above parameters for all the suitable turns ratios in Flyback applications
- We must follow just 5 rules to we select and connect up our transformer



6



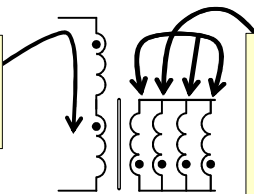
Rated Current as Our Winding Arrangement Changes

- The datasheet* specifies the rated current for just 1 coil out of 6
- They call this Rated Current Base or IR BASE (IR_Base) in RMS
 - EDF20 part datasheet shown earlier (Part No 74919654) has a IR_Base of 1.91A
 - Rated current is a thermal characteristic i.e. each one of the 6 coils can have 1.91A(RMS) without dissipating too much heat
 - e.g. if our secondary has 4 parallel paths all the way, then our overall rated current will be 4 x the specified IR_Base → $1.91 \times 4 = 7.64A$
- Note that paralleled windings on the *same core* are not separate inductances; they are just one inductance with more copper; just like having thicker wire → Rule 2: *when you parallel coils, the inductance stays the same*

Electrical Properties:

Properties		Value	Unit
Inductance Base ¹⁾	L _{BASE}	3.4	μH
Turns Ratio	n	1:1:1:1:1:1	
Rated Current Base ²⁾	I _{R BASE}	1.91	A
Saturation Current Base ³⁾	I _{SAT BASE}	4.18	A
DC Resistance Base ⁴⁾	R _{DC BASE}	30	mΩ
Voltage-μSecond Base ⁵⁾	fUdt _{BASE}	65.6	μVs
Leakage Inductance Base	L _{S BASE}	0.18	μH
Insulation Test Voltage	V _T	500	V (AC)

Example 1: Lowest number of current paths for primary is 1 ∴ Primary Rate Current IR_Pri = IR_Base = 1.91A



Lowest number of current paths for secondary is 4 current ∴ Secondary Rated Current IR_Sec = 4 x IR_Base = 1.91 x 4 = 7.64A

Datasheet data taken from <https://www.we-online.com/components/products/datasheet/749196540.pdf>

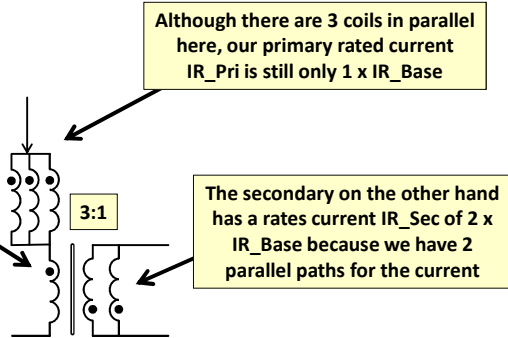


Rated Current as Our Winding Arrangement Changes

- Important: we must always use the lowest number of current paths in our primary or secondary for our calculations
- In other words if we have 3 coils in parallel but in series with the a single coil then our rated current is limited by that single coil
 - This is because IR_Base is a thermal characteristic of the device and that single coil will get hot

Rule 2: when you parallel coils on an off-the-shelf transformer, the inductance stays the same

Because the bottleneck is here, where we only have one single current path
This single path will get hot if we pass more than IR_Base through it!

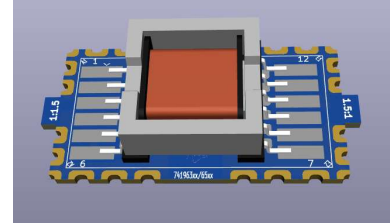


Although there are 3 coils in parallel here, our primary rated current IR_Pri is still only 1 x IR_Base

The secondary on the other hand has a rates current IR_Sec of 2 x IR_Base because we have 2 parallel paths for the current

All the Turns Ratios that We Can Have!

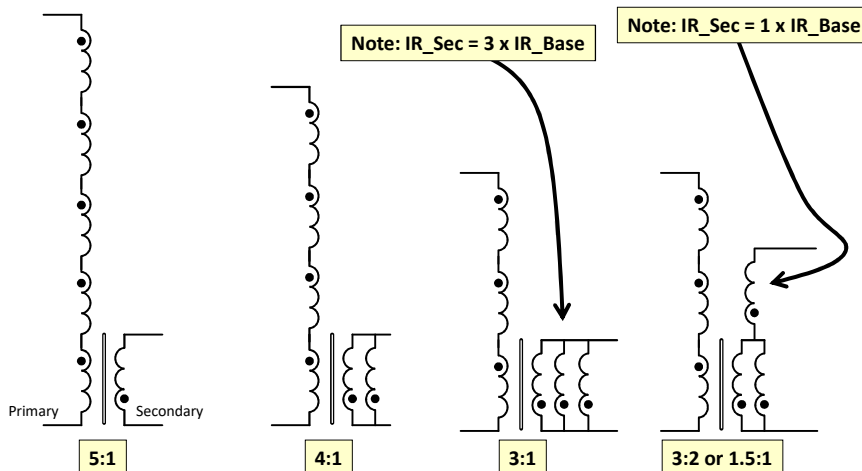
- As mentioned earlier, the 6 coils can be connected in many different ways to get the same turns ratio
 - And of course the pin numbers may vary depending on the transformer that you use → always consult device datasheet
- However, there are only a few options that are applicable/useable for a low voltage/low power DC-DC Flyback
- We have 6 coils, for our DC-DC Flybacks, we can have a total of 9 combinations :
 - 5:1, 4:1, 3:1, 3:2 (1.5:1),
 - 2:1 → 2 useful winding arrangements/versions (more in this later)
 - 1:1 → 3 useful winding arrangements/versions (more on this later)
- Of course, if we turn our transformer around we will also have:
 - 1:5, 1:4, 1:3, 2:3, 1:2
 - Often needed for step up applications



9

All the Turns Ratios that We Can Have

- To reduce confusion, let us go through ALL the combinations
 - But we just need to follow a handful of simple rules



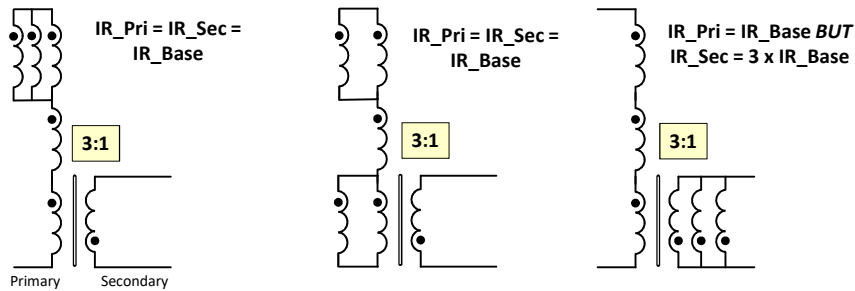
- Rule 1: Pinout will vary depending on the device and your layout, but the dot convention MUST stay the same
- Rule 2: When you parallel coils, the inductance stays the same but rated current increases

Rule 3: Do not leave coils unconnected as it will increase your losses

11

What is the Best Way to Parallel the Coils?

- We 6 coils; we can't have any coils unconnected (Rule 3) but which set up gives us the highest rated primary current IR_{Pri} and highest rated secondary current IR_{Sec} ?



Rule 4: Parallel the coils such that you maximise the number of current paths in either primary or secondary

For step down applications, we normally would like to maximize the current in the secondary

Reminder:

Rule 1: Stick to the dot convention

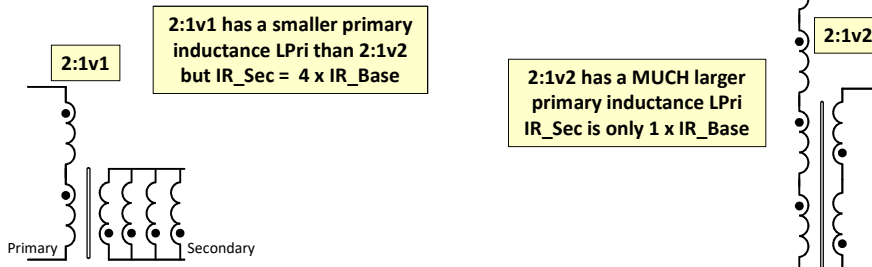
Rule 2: For paralleled coils the inductance stays the same but the rated current increases

Rule 3: We can not leave any coils unconnected

12

Back to All the Turns Ratios that We Can Have

- We have looked at 5:1, 4:1, 3:1, 3:2, so let us now look at 2:1
 - We can have more than 1 useful version of 2:1
- For 2:1v1
 - Primary Rated Current = IR_{Base} i.e. 1.92A from the datasheet
 - Secondary Rated Current = $IR_{Base} \times 4 = 1.92 \times 4 = 7.68A$
- For 2:1v2
 - Primary Rated Current $IR_{Pri} = IR_{Base}$ i.e. 1.92A (datasheet)
 - Secondary Rated Current $IR_{Sec} = IR_{Base} \times 1 = 1.92A$

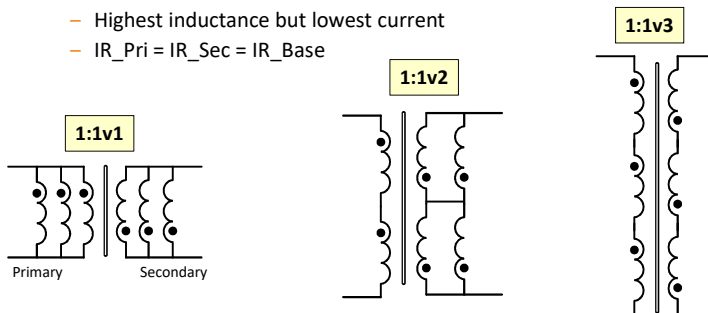


We will explain how to calculate inductances shortly but first let us look at our last remaining turns ratio \rightarrow 1:1

13

Finally 1:1

- For 1:1v1
 - Smallest inductance (compared to the other version) but biggest IR_Pri & IR_Sec
 - IR_Pri = IR_Sec = 3 x IR_Base
- For 1:1v2
 - Medium Inductance, medium IR_Pri & IR_Sec
 - IR_Pri = IR_Base, IR_Sec = 2 x IR_Base
- For 1:1v3
 - Highest inductance but lowest current
 - IR_Pri = IR_Sec = IR_Base



Primary and secondary inductances of 1:1v3 are 9 times bigger than those of 1:1v1

But how does putting coils in series on the same core impact their inductance?

We will talk about that next

14

How Do We Calculate our Primary and Secondary Inductances?

- The datasheet specifies the inductance of one coil LBASE
 - In this case LBASE is 3.4uH → this means we have 6 identical 3.4uH coils wound on the same core
 - We already know that if we parallel these coils the inductance stays the same but the rated current increases
- But what if we put them in series to get different turns ratios?
- We know from school level physics that the inductance of a coil L is proportional to the “square of the number of turns”:

$$L = k \times (n)^2 \rightarrow = \text{LBASE}$$

- Where n is the number of turns for ONE coil
- and everything else in our case is constant (because we have fixed them)
- So if we connect 2 coils in series we double the number of turns and our inductance goes up by a factor of 4 (i.e. 2²) and if we connect 3 in series our inductance goes up by a factor of 9 (i.e. 3²) and so on

Electrical Properties:

Properties		Value	Unit
Inductance Base ¹⁾	L _{BASE}	3.4	µH
Turns Ratio	n	1:1:1:1:1:1	
Rated Current Base ²⁾	I _{R BASE}	1.91	A
Saturation Current Base ³⁾	I _{SAT BASE}	4.18	A
DC Resistance Base ⁴⁾	R _{DC BASE}	30	mΩ
Voltage-µSecond Base ⁵⁾	∫Udt _{BASE}	65.6	µVs
Leakage Inductance Base	L _{S BASE}	0.18	µH
Insulation Test Voltage	V _T	500	V (AC)

15



How Do We Calculate our Primary and Secondary Inductances?

- From previous slides we know that our total inductance for either primary or secondary is given by $L = k \times (n)^2$
- We also know that the inductance for 1 coil is given in the datasheet as $\therefore L_{BASE} = k \times (n)^2$
 - In our case = 3.4uH
- If we connect 2 coils in series we double the number of turns and our inductance becomes $L = k \times (2n)^2$ or $L = k \times (n)^2 \times 2^2$
 - Or $L_{BASE} \times 2^2 \rightarrow$ in our case $3.4\mu H \times 4 = 13.6\mu H$
- If we connect 3 in series we triple n $\rightarrow (3n)^2$
 - Or $L_{BASE} \times 3^2 \rightarrow$ in our case $3.4\mu H \times 9 = 30.6\mu H$

Electrical Properties:

Properties		Value	Unit
Inductance Base ¹⁾	L_{BASE}	3.4	μH
Turns Ratio	n	1:1:1:1:1:1	
Rated Current Base ²⁾	$I_{R\,BASE}$	1.91	A
Saturation Current Base ³⁾	$I_{SAT\,BASE}$	4.18	A
DC Resistance Base ⁴⁾	$R_{DC\,BASE}$	30	m Ω
Voltage- μ Second Base ⁵⁾	$\int Udt_{BASE}$	65.6	$\mu V s$
Leakage Inductance Base	$L_{S\,BASE}$	0.18	μH
Insulation Test Voltage	V_T	500	V (AC)

Rule 5: Total inductance is given by:
 $L_{pri} = L_{BASE} \times (\text{Number of primary coils connected in series})^2$
 $L_{sec} = L_{BASE} \times (\text{Number of secondary coils connected in series})^2$

* L is the inductance of a long solenoid, N is the number of turns, μ_0 is the permeability free space A is the and l is the length



All Data We Need for EVERYTHING in One Spreadsheet

Order Code	Datasheet	Lpri (μH)	IR Pri (A)	Primary ISAT (A)	IR Sec (A)	Lsec (μH)	VT (V (AC))	Size
74919734	SPEC	52.5	3.20	3.90	2.20	3.70	500	ETD39
74919733	SPEC	935.0	3.20	1.98	2.20	49.40	500	ETD39
74919732	SPEC	932.5	3.20	0.82	2.20	77.30	500	ETD39
74919731	SPEC	212.5	3.20	0.38	3.20	128.5	500	ETD39
74919724	SPEC	350.00	2.50	3.49	2.50	22.00	500	ETD39
74919723	SPEC	302.50	2.50	1.90	2.50	36.10	500	ETD39
74919722	SPEC	1735.00	2.50	0.80	2.50	69.40	500	ETD39
74919721	SPEC	2845.00	2.50	0.40	2.50	10.8	500	ETD39
749196547	SPEC	85.00	2.30	4.72	2.30	3.40	500	ETD39
749196537	SPEC	107.50	2.30	3.89	2.30	4.30	500	ETD39
749196527	SPEC	132.50	2.30	3.37	2.30	5.30	500	ETD39
749196517	SPEC	247.50	2.30	1.62	2.30	9.90	500	ETD39
749197141	SPEC	375.00	2.20	3.72	2.20	15.00	500	ETD39
749197131	SPEC	607.50	2.20	2.20	2.20	24.30	500	ETD39
749197121	SPEC	1157.50	2.20	1.00	2.20	46.30	500	ETD39
749197111	SPEC	1877.50	2.20	0.48	2.20	75.10	500	ETD39
749196540	SPEC	85.00	1.91	5.02	1.91	3.40	500	ETD39
749196530	SPEC	107.50	1.91	3.49	1.91	4.30	500	ETD39
749196520	SPEC	132.50	1.91	3.04	1.91	5.30	500	ETD39
749196510	SPEC	247.50	1.91	1.40	1.91	9.90	500	ETD39
749196541	SPEC	190.00	1.70	2.95	1.70	7.60	500	ETD39
749196531	SPEC	242.50	1.70	2.64	1.70	9.70	500	ETD39
749196521	SPEC	300.00	1.70	2.08	1.70	12.00	500	ETD39
749196511	SPEC	567.50	1.70	0.59	1.70	22.30	500	ETD39
749196508	SPEC	190.00	1.35	3.58	1.35	6.60	500	ETD39
749196503	SPEC	222.5	1.35	2.95	1.35	8.70	500	ETD39

These rules are becoming cumbersome so let us put everything that we need for every combination in just one spreadsheet

Please download Biricha's Flex Transformer Spreadsheet from: www.biricha.com/flex

So far we have given all the equations and explained exactly where they come from but we recommend using our Flex Transformer Spreadsheet

The final parameter that we need for Flyback design is Primary Saturation Current

For completeness we will explain this in detail also

...

But we still recommend using the spreadsheet! ☺



How Do We Calculate Our Primary Saturation Current?

- The datasheet specifies our Saturation Base Current ISAT_BASE
 - In this cause it is 4.18A *PEAK* not RMS!
- Unlike “Rated Current” which is a thermal parameter, “Saturation Current” is determined by the maximum flux that our core can handle
 - Saturation current is determined by our PEAK current and not RMS
 - If we saturate our core, it stops acting like an inductor and things will go very wrong!
 - It’s a little bit like a washing up sponge that is completely saturated with water → it stops acting like a sponge and water will go everywhere!
- Remember from school physics that our flux is proportional to our Amp-turns*
 - If our core saturates with 100A and only 1 turn, we can also saturate with 1A and 100 turns
- We can consider** ISAT_BASE as the current that will saturate our core when we have the maximum number of turns, i.e. when all 6 coils are connected in series
 - But if we only use 3 coils in series and nothing else, then we have halved our number of turns, therefore we can have twice as much current before saturation

* Flux ϕ (in Webers) = Magneto-motive Force (MMF in A.turns) / Reluctance (A/Weber)

** this is for ease of explanation only, in practice it is measured a different way, please consult the datasheet

Properties		Value	Unit
Inductance Base ¹⁾	L_{BASE}	3.4	μH
Turns Ratio	n	1:1:1:1:1:1	
Rated Current Base ²⁾	$I_{R,BASE}$	1.91	A
Saturation Current Base ³⁾	$I_{SAT,BASE}$	4.18	A
DC Resistance Base ⁴⁾	$R_{DC,BASE}$	30	m Ω
Voltage- μ Second Base ⁵⁾	$f_{Udt,BASE}$	65.6	μVs
Leakage Inductance Base	$L_{S,BASE}$	0.18	μH
Insulation Test Voltage	V_T	500	V (AC)

Yes it is be bit confusing → let us elaborate with an example in the next slide

18



How Do We Calculate Our Primary Saturation Current?

- First, please remember that in a Flyback transformer* when the current in the primary winding is flowing, there is no current flowing in the secondary “winding”
 - There is no net contribution to the Amp-Turns from the secondary and therefore, we only need to consider the primary currents and coils for saturation purposes
- From previous slides we know that paralleled coils on the same core do not increase our number of turns → it is just like using thicker wire
 - We ignore all parallel coils and only consider series coils in our primary
- If we use all 6 coils (in series on the primary), we can have our maximum number of turns and therefore minimum saturation current
 - Primary Saturation Current, ISAT_PRI = ISAT_BASE
- If we have 5 coils in series on the primary the we have only used 5/6th of our maximum number of turns, therefore we can increase our ISAT_PRI by 6/5 ... and so on
 - Primary Saturation Current ISAT_PRI = 5/6 x ISAT_BASE = 1.2 x ISAT_BASE
 - e.g. if ISAT_BASE in datasheet = 4.18A, then ISAT_PRI = 1.2 x 4.18A = 5.0A
- And so on ...

Rule 6: For Flyback transformers

$$ISAT_Pri = 6 \times ISAT_BASE / \text{Primary coils in series}$$

...
But please use the spreadsheet! ☺

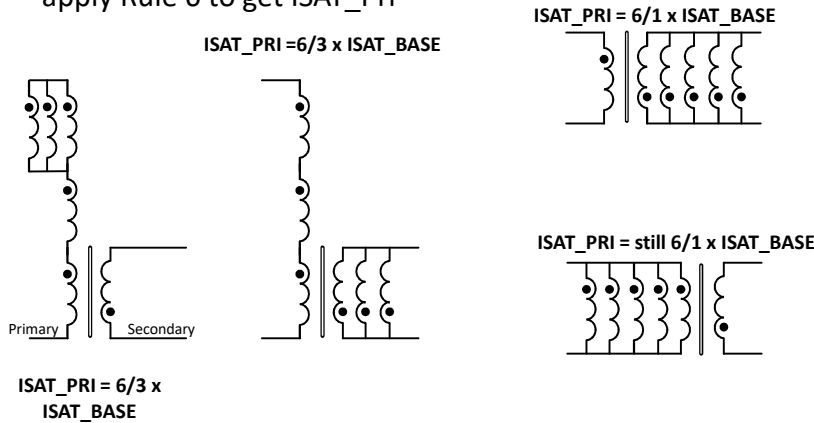
* Ideal conditions, ignoring any cross conduction and turn on/off times

19



How Do We Calculate Our Primary Saturation Current

- Only count series connected coils on the primary and then apply Rule 6 to get ISAT_Pri



20



Summary of Rules

- So far have given
 - Full explanation +
 - All necessary equations and where they come from +
 - Flex Transformer Spreadsheet containing most of the useful combinations of turns ratios and coils + connections
 - 6 simple rules to follow ...
- The Rules
 - Rule 1: Stick to the dot convention
 - Rule 2: When we connect coils in parallel on the same core the inductance stays the same and the rated current increases
 - Rule 3: We can not leave any coils unconnected
 - Rule 4: Parallel the coils such that you maximum the number of current paths in either primary or secondary
 - Rule 5: Total inductance is given by:
 - $L_{pri} = L_{BASE} \times (\text{Number of primary coils connected in series})^2$
 - $L_{sec} = L_{BASE} \times (\text{Number of secondary coils connected in series})^2$
 - Rule 6: For Flyback transformers
 - $ISAT_Pri = 6 \times ISAT_BASE / \text{Primary coils in series}$

*Let us now look at a few design examples
We will use Biricha WDS and Flex Transformer Spreadsheet to do all our calculations*

*You can download everything for free from
www.biricha.com/omicron &
www.biricha.com/flex*

21



Design Examples and Calculations with WDS and Flex Transformer Spreadsheet

- Biricha WDS Automatic Power Supply Design Software that allows you to design and stabilise power supply control loops down lot component level
 - Detailed control loop design is covered in our Analog Power Supply Design Workshop, for now we will just use WDS
- It calculates all necessary voltages, currents and stresses on power devices
 - So you can select your power components
- It calculates the size of our output capacitor based on our ripple and step response requirements
- Finally it provides us with IR_Pri, IR_Sec, ISAT_Pri, Turns Ratio, Transformer Primary and Secondary Inductances (Lpri and Lsec respectively)
 - In other words ALL the parameters that we need to select our off the shelf transformer
- We can then use our Flex Transformer spreadsheet to see if we can find a suitable of-the-shelf component (or calculate by hand using the techniques shown in this presentation)
 - If you can't find one then you have to get a custom transformer

You can download WDS and our Flex Transformer spreadsheet for free from
www.biricha.com/omicron &
www.biricha.com/flex

22



Example 1

Step 1 : In the Specification tab, fill in your Vin, Vout and Iout

Followed by acceptable ripple on the output and how fast you want your power supply to recover after a 50% step change in load → this determines the size of the output cap

Leave everything else as default for now

Flyback_Example1_48V_to_9V_1A .wds - Biricha WDS - Power Supply Design Tool

The screenshot shows the 'Converter Specification' tab in the Biricha WDS software. The 'Topology' is set to 'Flyback CCM'. The 'Output voltage isolated from primary side' is set to 'Isolated'. The 'Input Supply' section shows a Maximum voltage of 52V, a Nominal voltage of 48V, and a Minimum voltage of 44V. The 'Output' section shows a Maximum Current of 1A and a Voltage of 9V. The 'Output voltage ripple / overshoot' section shows a Voltage Ripple (pk-pk) of 0.2% and a Voltage Ripple (pk-pk) of 18 mV. The 'Load Step from 100% to' is set to 50%.

- 48V in - Isolated 9V,1A out
- Fs = 200kHz but we may have to tweak a little bit to be able to buy an off-the-shelf transformer
- WDS to automatically calculate all values needed for silicon, control loop, output caps and transformer

23



Example 1 – Isolated Flyback 48V In – 9V@1A Out

- WDS calculates our turns ratio based our maximum duty cycle
- Flyback topology is quite forgiving with regard to the maximum duty cycle
 - Therefore we can iteratively adjust this until you get a turns ratio that is available in our of-the-shelf transformers
- Max Duty can be set to anything between 30 to 55% and still get reasonable results
 - Let us start with 50% but if necessary we can change this to get near to our available turns ratios
 - Please remember that we can only have 5:1, 4:1, 3:1, 3:2 (1.5:1), 2:1 (2 versions) and 1:1 (3 versions)

Step 2: For now set the Max duty limit to 50%

We will then iterate and adjust this to find a available off-the-shelf transformer

Duty Cycle (per switch)	
Maximum Duty Limit	50 %
Minimum Duty Limit	0 %
Maximum	49.992 %
Nominal	47.807 %
Minimum	45.805 %

24



Example 1 – Isolated Flyback 48V in – 9V@1A Out

- As we play with our turns ratio; WDS then will recalculate EVERYTHING else that you need to select your transformer

Step 3: Iterate reasonable turns ratios and then use WDS values to find the correct transformer from the Spreadsheet

The screenshot shows the WDS software interface with two panels. The left panel shows transformer specifications with the following values:

Parameter	Value
Recommended (Np:1)	4.548
Current Ripple (pk-pk)	80 %
Primary Side Inductance	308.575 μH
Recommended Leakage	12.343 μH
Coupling Cap Ripple	n/a %
Coupling Capacitor	n/a μF
Cap RMS Current	n/a mA
Volt-μSecond Product	129.25 V·μS
Mag. Current (pk-pk)	n/a mA
Primary Current (pk)	0.605519 A
Primary RMS Current	0.318 A
Secondary Current (pk)	n/a A
Secondary RMS Current	1.447 A
DCM/CCM Boundary	3.587 W

The right panel shows similar specifications with a turns ratio of 4:

Parameter	Value
Recommended (Np:1)	4
Current Ripple (pk-pk)	80 %
Primary Side Inductance	268.642 μH
Recommended Leakage	10.746 μH
Coupling Cap Ripple	n/a %
Coupling Capacitor	n/a μF
Cap RMS Current	n/a mA
Volt-μSecond Product	129.25 V·μS
Mag. Current (pk-pk)	n/a mA
Primary Current (pk)	0.648168 A
Primary RMS Current	0.329 A
Secondary Current (pk)	n/a A
Secondary RMS Current	1.404 A
DCM/CCM Boundary	3.585 W

Annotations in yellow boxes with red arrows point to the following values:

- Turns ratio N**: Points to the value 4 in the right panel.
- Primary Inductance LPri**: Points to the Primary Side Inductance value of 308.575 μH in the left panel.
- Peak Primary Current → ISAT_Pri**: Points to the Primary Current (pk) value of 0.605519 A in the left panel.
- Primary RMS Current → IRMS_Pri**: Points to the Primary RMS Current value of 0.318 A in the left panel.
- Secondary RMS Current → IR_Sec**: Points to the Secondary RMS Current value of 1.447 A in the left panel.

25



Example 1 – Isolated Flyback 48V in – 9V@1A Out

Order Code	Datasheet	Lpri (μH)	IR Pri (A)	Primary ISAT (A)	IR Sec (A)	Lsec (μH)	VT (V (AC))	Size
749196248	SPEC	105.60	0.80	2.40	1.60	6.60	1500	ER14.5/6
749196238	SPEC	132.80	0.80	1.95	1.60	8.30	1500	ER14.5/6
749196228	SPEC	185.60	0.80	1.41	1.60	11.00	1500	ER14.5/6
749196218	SPEC	345.60	0.80	0.72	1.60	21.00	1500	ER14.5/6
749196141	SPEC	136.00	0.55	1.44	1.10	8.50	500	ER11/5
	SPEC	174.40	0.55	1.10	1.10	10.90	500	ER11/5
	SPEC	235.20	0.55	0.81	1.10	14.70	500	ER11/5
	SPEC	438.40	0.55	0.33	1.10	27.40	500	ER11/5
749196148	SPEC	136.00	0.50	1.53	1.00	8.50	1500	ER11/5
749196138	SPEC	174.40	0.50	1.20	1.00	10.90	1500	ER11/5
749196128	SPEC	235.20	0.50	0.83	1.00	14.70	1500	ER11/5
749196118	SPEC	438.40	0.50	0.44	1.00	27.40	1500	ER11/5

Click on SPEC for datasheet

We have found our part ☺
Our transformer selection is complete!

- Look for WDS values in this order:
 - $N \rightarrow 4:1$
 - $IR_Pri \geq 0.329A$
 - $Lpri \geq 268.642\mu H$
 - $IR_Sec \geq 1.404A$
 - $ISAT_Pri \geq 0.329A$

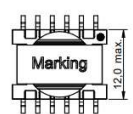
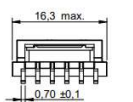
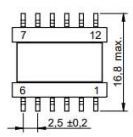
Step 4: Use WDS calculated data to quickly find a suitable transformer. If you do not find a suitable transformer, try 3:1 or 5:1 or change what ever else you can change

Np/Ns = User_Defined Np/Ns = 5:1 **Np/Ns = 4:1** Np/Ns =



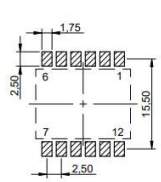
WE 749196218 Transformer Datasheet*

Dimensions: [mm]

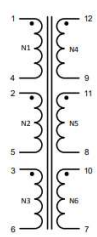


Of course we have everything in the Flex Transformer spreadsheet, but using the material in this presentation we can also calculate by hand

Recommended Land Pattern: [mm]



Schematic:



Electrical Properties:

Properties	Test conditions		Value	Unit	Tol.
Inductance Base	100 kHz/ 100 mV	L_{BASE}	21.6	μH	±20%
Turns Ratio		n	1:1:1:1:1:1		
Voltage-μSecond Base		$\int U_{dct} dt_{BASE}$	63.4	μVs	max.
Rated Current Base	$\Delta T = 40 K$	I_{R_BASE}	0.8	A	typ.
Saturation Current Base	$ dI/dt < 10 \%$	I_{SAT_BASE}	0.48	A	typ.
DC Resistance Base	@ 20 °C	R_{DC_BASE}	200	mΩ	max.
Leakage Inductance Base	100 kHz/ 1 V	L_S_BASE	0.17	μH	typ.
Insulation Test Voltage	all windings	U_T	1500	V (AC)	

WE-FLEX HV Flexible Transformer for DC/DC Converter

Order Code: 749196218

DATE: 2017-01-05

* Full datasheet on: <https://www.we-online.com/components/products/datasheet/749196218.pdf>



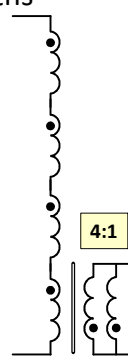
Calculations by Hand – For Completeness Only!

- Primary has 4 coils in series and only 1 current path
 - $L_{pri} = L_{BASE} \times 4^2 = 21.6\mu H \times 4^2 = 345.6\mu H$
 - $I_{R_Pri} = I_{R_Base} = 800mA$
 - $I_{SAT_Pri} = 6/4 \times I_{SAT_Base} = 1.5 \times 0.48A = 720mA$
- Secondary has 2 coils in parallel and 2 current paths
 - $I_{R_Sec} = 2 \times I_{R_Base} = 1.6A$
- All 6 coils are connected
 - No dangling coils!
- Dot convention is correct for our design

Our Transformer selection is complete! We still have to select our other components + design the control loop ... WDS will do all of these
More details on our Analog Power Supply Design Workshop

Electrical Properties:

Properties	Test conditions		Value	Unit	Tol.
Inductance Base	100 kHz/ 100 mV	L_{BASE}	21.6	μH	$\pm 20\%$
Turns Ratio		n	1:1:1:1:1:1		
Voltage- μ Second Base		f_{UDT_BASE}	63.4	μVs	max.
Rated Current Base	$\Delta T = 40 K$	I_{R_BASE}	0.8	A	typ.
Saturation Current Base	$ dI/dt < 10 \%$	I_{SAT_BASE}	0.48	A	typ.
DC Resistance Base	@ 20 °C	R_{DC_BASE}	200	m Ω	max.
Leakage Inductance Base	100 kHz/ 1 V	L_{S_BASE}	0.17	μH	typ.
Insulation Test Voltage	all windings	U_T	1500	V (AC)	



* Full datasheet on: <https://www.we-online.com/components/products/datasheet/749196218.pdf>



Completing the Design with WDS

- Silicon stresses and selection

Specification	Transformer	Semiconductors
Primary Switch		
"On" Resistance	< 20	20 m Ω
Rise Time	< 6.03	6.03 ns
Fall Time	< 6.03	6.03 ns
Parasitic Cap (Coss)	< 41.494	41.494 pF
Peak Switch Voltage	95.661	V
Average Switch Current	0.22	A
RMS Switch Current	0.318	A
Peak Switch Current	0.606	A
Conduction Losses	0.002	W
Switching Losses	0.076	W
Recommended values for calculations		

Diode/Switch		
Forward Voltage Drop	0.6	0.6 V
Peak Voltage Stress	20.398	V
Average Current	1	A
RMS Current	1.447	A
Peak Current	2.754	A
Conduction Losses	0.6	W
Recommended values for calculations		

Completing the Design with WDS

- Output Caps selection and stresses
 - Calculated based on Vout ripple requirements and transient response that we specified earlier in the specification tab

Output Filter Capacitor		
C0 Capacitance	639.391	639.391 μF
C0 ESR	10.322	10.322 $\text{m}\Omega$
C0 ESR Zero	24114.385	Hz
Specified Overshoot	1800	mV
Actual Overshoot	62.443	mV
Specified Ripple (pk-pk)	18	mV
Actual Ripple (pk-pk)	20.6	mV
RMS Current	1.046	A
Ripple Current (pk-pk)	2.754	A
Peak Voltage	9.025	V
Power Dissipation	11.298	mW

30

Completing the Design with WDS

- Current sense, slope compensation and control loop design

Current Sense and Slope Compensation		
Current Sense Gain <	1.682	1.682 V/A
Magnetizing "Free" Ramp	0	V(pk-pk)
Optimal External Ramp	0.743	V(pk-pk)
Amount of Ramp to Add	0.743	0.743 V(pk-pk)
Ramp Slope	0	mV/usec
V. on Current Sense Pin	1.743	V

Controller Poles and Zeros		
<input checked="" type="radio"/> Automatic placement <input type="radio"/> Manual placement		
Pole at the origin	5212.142	5212.142 Hz
First Pole	24114.385	24114.385 Hz
Second Pole	n/a	n/a Hz
First Zero	1076.615	1076.615 Hz
Second Zero	n/a	n/a Hz

Output Capacitor	Controller Design	Analog (Isolated)
Controller Type		
Type II		
$H_c(s) = \frac{a_{p0}}{s} \frac{\left(\frac{s}{a_{z1}} + 1\right)}{\left(\frac{s}{a_{p1}} + 1\right)}$		
<input checked="" type="radio"/> Op-amp <input type="radio"/> Transconductance Amp		
Transconductance Factor gm n/a $\mu\text{Mho}/\mu\text{S}$		

**We talk about these in detail in our
Analog Power Supply Design
Workshops**

www.biricha.com

31

Completing the Design with WDS

- Opto-coupler, programmable reference and Isolated loop design does to component level

Optocoupler		Divider and Error Amplifier	
Isolation Configuration: CNY17-3 5: *@ Vpullup=8V, Rpullup=2.2		Error Amp Configuration: LMV431	
Let Biricha characterize your opto			
Max. CTR	185 %	Vref - Min. Programmable Ref Voltage	1.24 V
Typ. CTR	123 %	Desired Ibias - Min. Cathode Current	0.2 mA
Min. CTR	61 %	Rbias	6.95 kΩ
Vpullup (Primary)	8 V	Actual Ibias	0.2 mA
Vce(sat) (Primary)	0.25 V	Max. Cathode Current	3.064228 mA
Copto (Primary)	2.8 nF	Desired Ipd - Current through divider	0.25 mA
Desired Ic Max. (Primary)	3.523 mA	R1	31.04 kΩ
Rpullup (Primary)	2.2 kΩ	Actual Ipd	0.25 mA
Actual Ic Max. (Primary)	3.523 mA	R1 Power Dissipation	1.94 mW
Calculated If (Secondary)	2.864 mA	Rb	4.96 kΩ
Vf (Secondary)	1.39 V	Rb Power Dissipation	0.31 mW

Full details on our Analog Power Supply Design Workshop

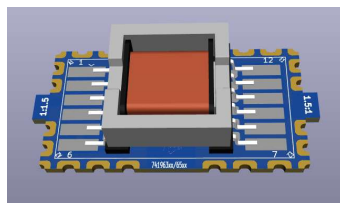
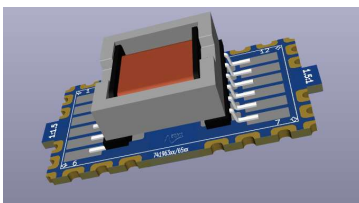
or see

Christophe Basso's great book on the subject
ISBN:978-1-608007-557-7

32

Coming Soon: New Online Workshop from Biricha

- Fast Flyback Design Online Workshop
 - Online workshop but with hands-on element
 - The objective is to help electronic engineers with little/no background in power supply design to design their DC/DC Flybacks FAST!
 - At the end of the workshop the attendees will have designed their Flyback “down to the very last component” based on “their own spec”
- To allow a dynamic specification based on the customers' own applications, we have designed set of Würth Elektronik Flex transformer daughter cards with all the 9 turns ratios
 - All suitable transformer configuration will be available on daughter cards

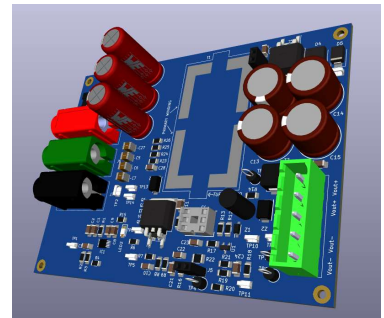


33



Coming Soon: New Online Workshop from Biricha

- We have also designed a DC/DC Flyback whose specification can be changed by the user
 - Will be available for sale from many major suppliers
 - Isolated Flyback - $V_{in} \leq 52V$, V_{out} 3.3 to 50V, $P_{out} \sim 10W$
 - Multiple footprints for many of the components
 - Includes robust silicon with high ratings as well as protection/snubbers/clamp
- WDS + Biricha Flex Transformer spreadsheet can calculate all components based on the users specification and then the correct transformer daughter card can be soldered directly on the board
- Detailed online material, multiple design examples, workbooks, videos, tools, WDS, spreadsheets etc. will guide the students in a step-by-step manner to designs their power supply and get their product out of the door in record time
 - Not a single component omitted ☺
- The attendees can then design and test their power supply for their product with full confidence that at least there are no hardware bugs
- The online workshop will be available on teachable.com soon
 - We start with Isolated Flyback; other popular topologies will follow



34



***To be kept informed please register on
our mailing list***

www.biricha.com/news

***The End of Presentation
Thank you very much for your time***

35