# Inductor Resonances and EMI

# OMICRON 14th Power Analysis & Design Symposium



Arturo Mediano
Founder & EMI/EMC magician
HF MAGIC LAB S.L.
amediano@hfmagiclab.com

Organized by,





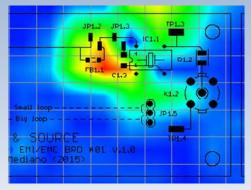
Prof. **Arturo Mediano**I3A, **UNIVERSITY OF ZARAGOZA** (SPAIN) amediano@unizar.es

2025

April 9<sup>th</sup>, 2025



## A High Frequency Lab for design, diagnostic, troubleshooting and training



Interferences (EMI)
Electromagnetic Compatibility (EMC)
Signal Integrity (SI)
Radiofrequency(RF)

Contact: Arturo Mediano
HF MAGIC LAB S.L.
Bari, 31. Edificio TechnoCenter. Oficinas 216-217
50197 Zaragoza (Spain)
amediano@hfmagiclab.com

#### About the speaker ...

#### ARTURO MEDIANO - HF MAGIC LAB S.L.



- Faculty member at the University of Zaragoza (SPAIN) for 30 years
- Founder HF Magic Lab S.L.
- Senior Member IEEE
- Chair IEEE EMC-S Spain Chapter
- Past Chair IEEE MTT-S Technical Committee 17
- amediano@unizar.es



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#### **Outline** ...

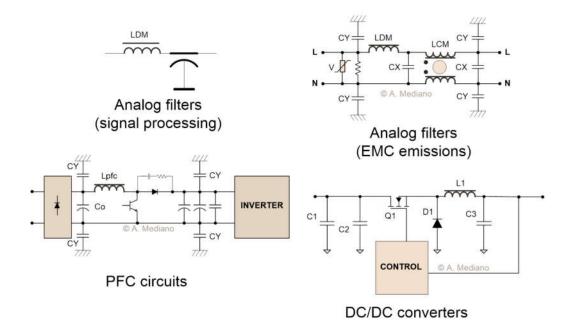
- 1. Introduction inductors everywhere.
- 2. Let's work with inductors in the ideal way.
- 3. Using cores.
- 4. Example Buck converter
  - Ideal design and simulation
  - Prototype
  - EMI results
  - Inductor characterization and model
  - Using LTSPICE to analyze the problems

#### 5. Conclusions



## Inductors ... a common component

#### INDUCTORS EVERYWHERE ....





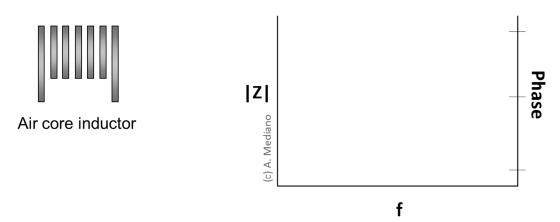
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## Inductors ... ideal way.

#### How to work with inductors .... IDEAL MODEL

Take a wire and create a simple model:





#### Inductors ... they are not ideal

# The ideal behavior of an inductor is a **fantasy**. Its parasitics define your EMI/EMC reality.

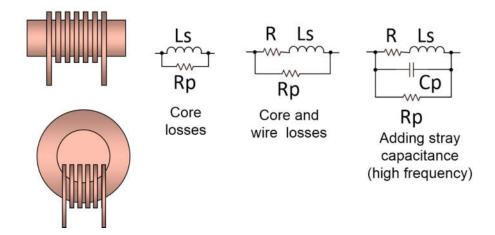


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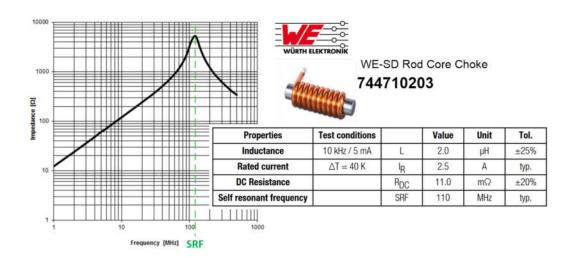
### Inductors ... using core.

A core is used for a higher inductance without the need for big sized inductors.





## Inductors ... commercial example



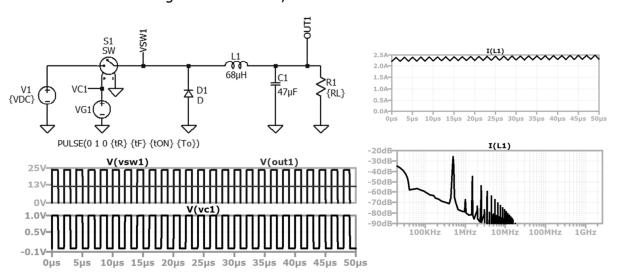


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### **Example:** Buck converter

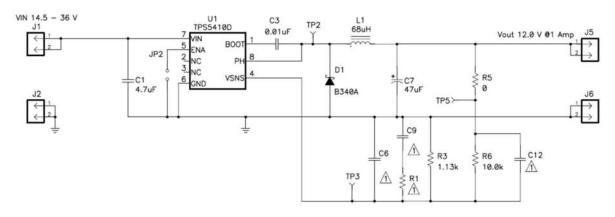
A Buck converter is designed for Vin=24V, Vout=12V and RL=5ohm





### **Example:** Buck converter and EMC

#### A prototype is built ...





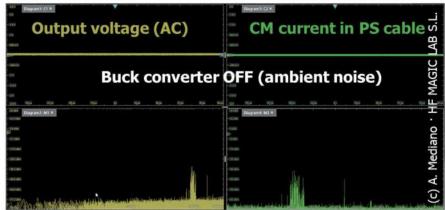
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#### **Example:** Radiated emissions

Let's measure output voltage and CM current in power supply cable:

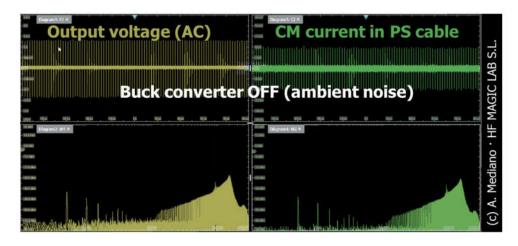






#### **Example:** Radiated emissions

Turn ON buck converter and radiated emissions fail in VHF because CM current in input voltage cable:



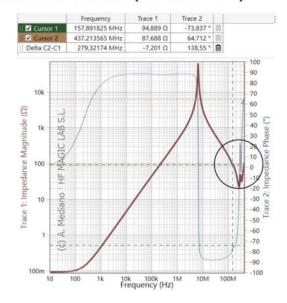


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## **Example:** Inductor characterization

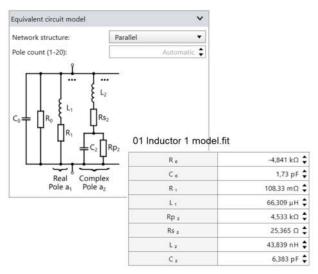
Let's measure response of our inductor (INDUCTOR #1):

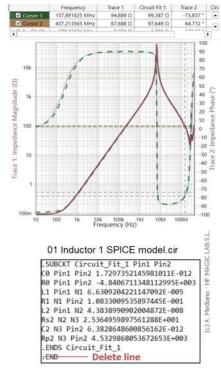




#### **Example:** Inductor model

#### Let's calculate inductor #01 model:







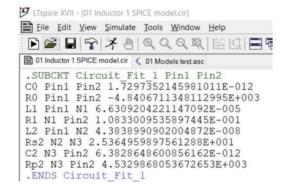
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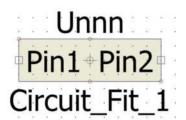


#### **Example: Inductor model**

#### Open the .CIR file with LTSPICE:



Use clik+right mouse button for crating symbol and save in project folder:

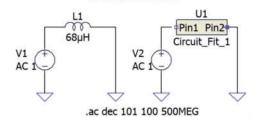




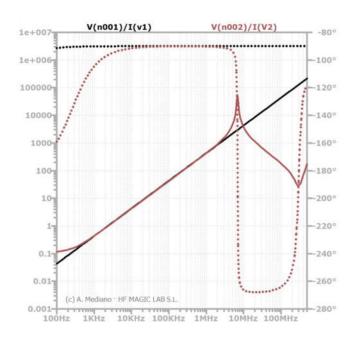
#### **Example:** Test model in simulation

#### Check model with LTSPICE:

01 Models test.asc



.SUBCKT Circuit\_Fit\_1 Pin1 Pin2
C0 Pin1 Pin2 1.7297352145981011E-012
R0 Pin1 Pin2 -4.8406711348112995E+003
L1 Pin1 N1 6.6309204221147092E-005
R1 N1 Pin2 1.0833009535897445E-001
L2 Pin1 N2 4.3838990902004872E-008
Rs2 N2 N3 2.5364959897561288E+001
C2 N3 Pin2 6.3828648600856162E-012
Rp2 N3 Pin2 4.5329868053672653E+003
.ENDS Circuit\_Fit\_1





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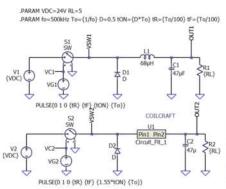




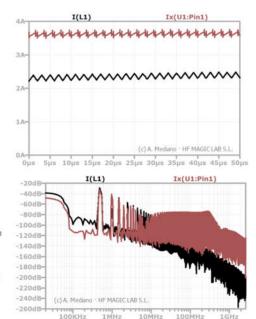
## **Example:** Test model in simulation

Create a model of the DC/DC converter with the "real" inductor:

02 Buck demo.asc



SUBCKT Circuit, FR. 1. Pin1 Pin2 CD Pin1 Pin2 1.7297352145981011E-012 RQ Pin1 Pin2 4-8.4906711348112995E+00: L1 Pin1 N1 6.6309204221147092E-005 R1 N1 Pin2 1.0833009535897445E-001 L2 Pin1 N2 4.3838990902004872E-008 R2 N2 N3 2.5364959897561288E-001 C2 N3 Pin2 6.3828648600856162E-012 RQ2 N3 Pin2 4.332986003672653E+003 ENDS Circuit FR. 1





## **THANK YOU!**



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